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HSF Property : HF

ROCKY2

Huron River Platform

MV Build

2010/03/16

2010-11-04	2010-ECU-023114	B
DATE	CHANGE NO.	REV

	EE	DATE	POWER	DATE	INVENTEC		
DRAWER	Chen, Leo	2010-06-14	Chen, Iray	2010-06-14	TITLE		
DESIGN	Chen, Michael	2010-06-14			RKY15HR		
CHECK	Wu, Bruce	2010-06-14			Project Name		
RESPONSIBLE		2010-06-14			SIZE = 3		
					FILE NAME : XXXX-XXXXXX-XX		
					P/N XXXXXXXXXXXX		
					VER :		
					SIZE CODE		
					A3	CS	1310A24121-0-MTR
					SHEET 1 OF 58		

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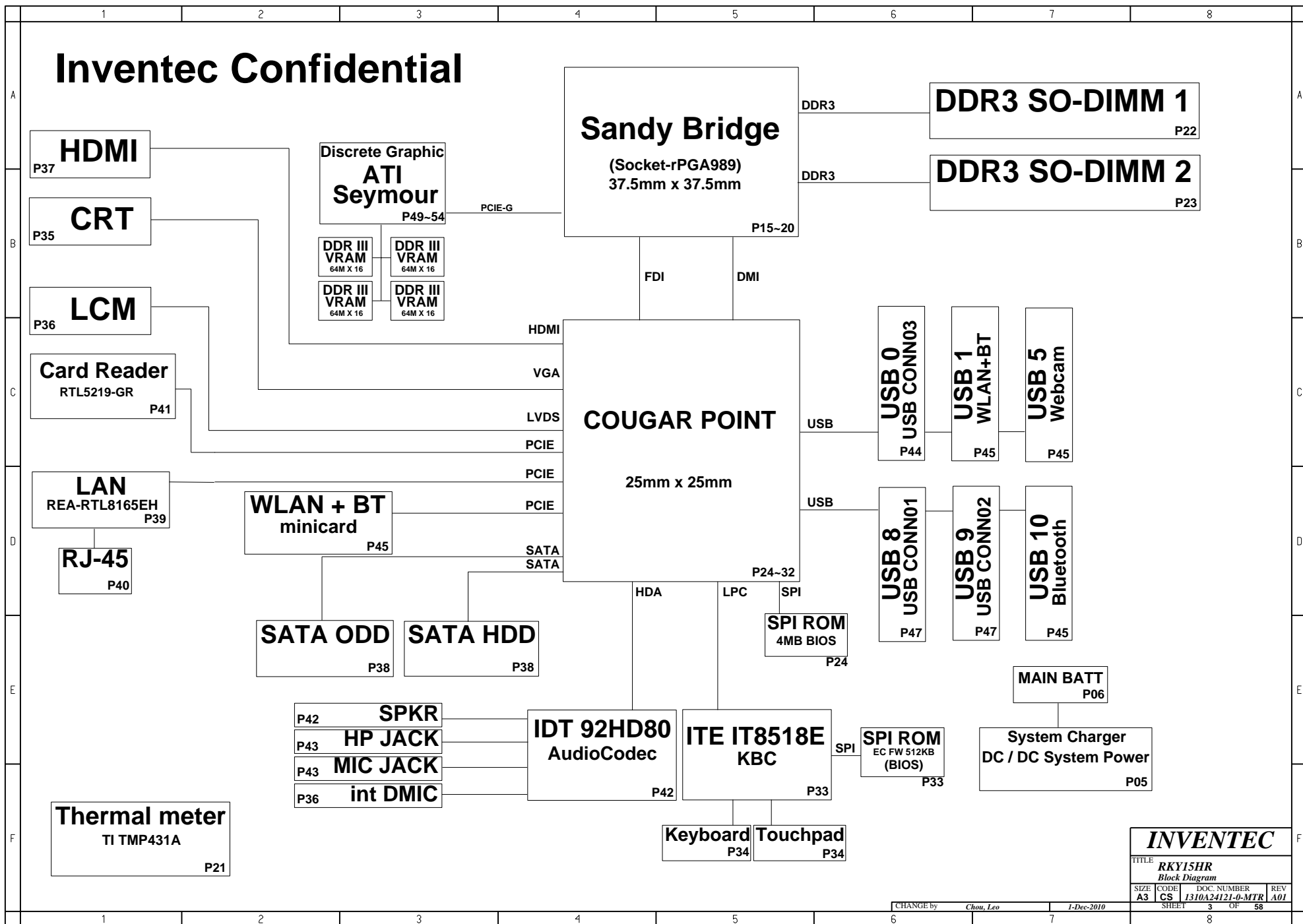
PAGE

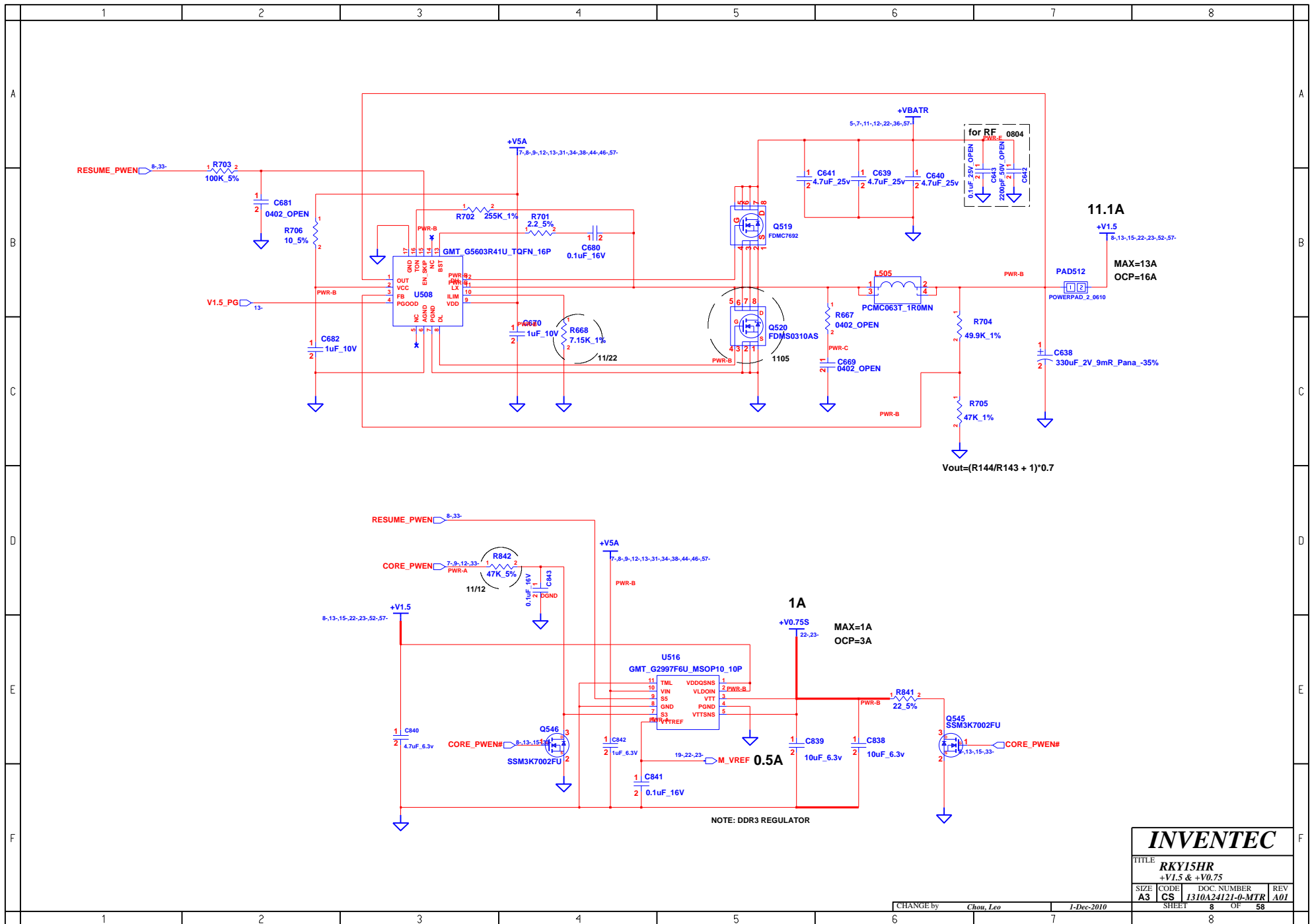
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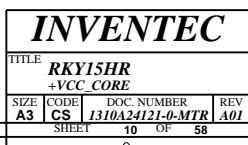
INVENTEC

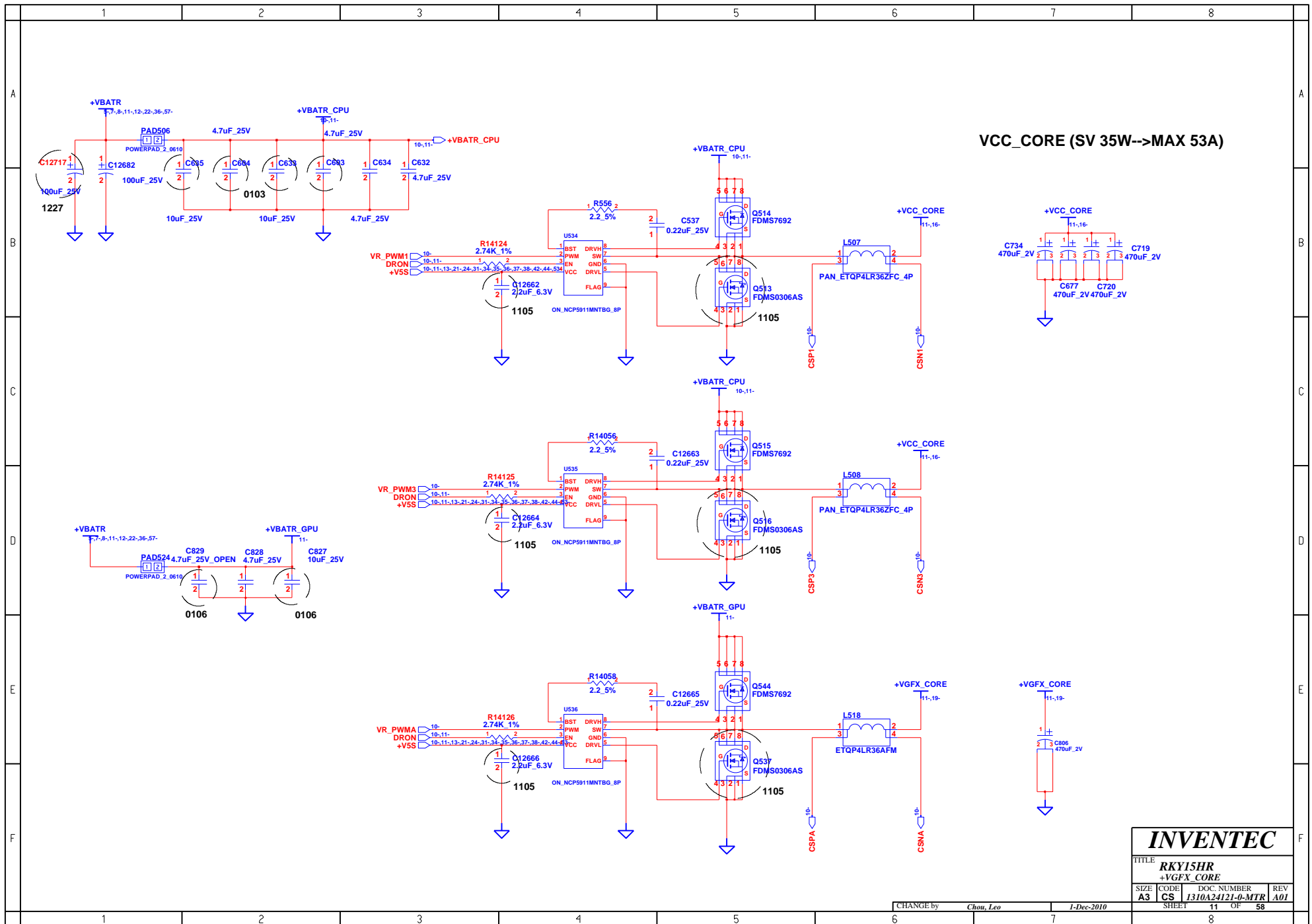
TITLE			
RKV15HR			
Index			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A24121-0-MTR	A01
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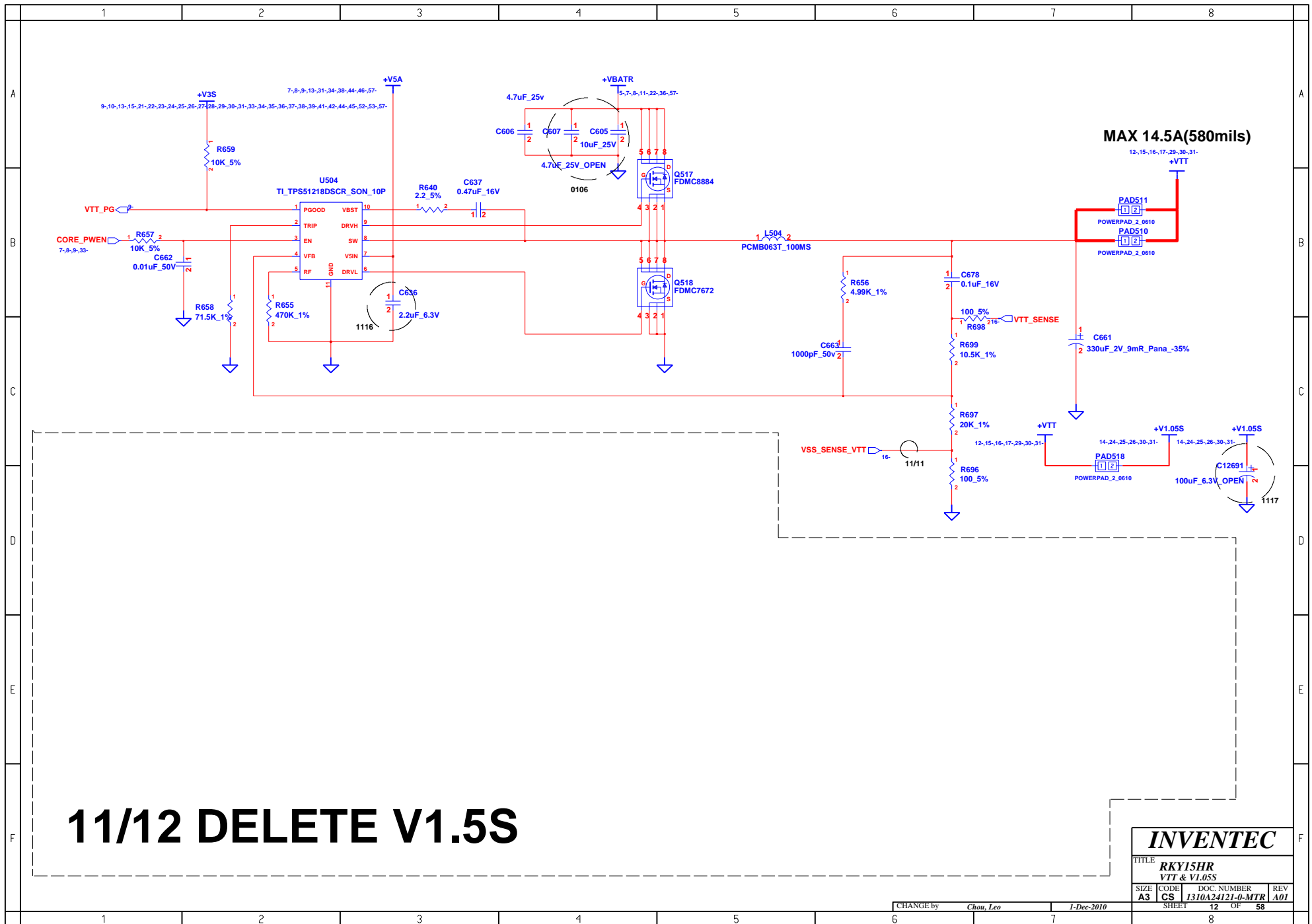
CHANGE by Chou, Leo 1-Dec-2010

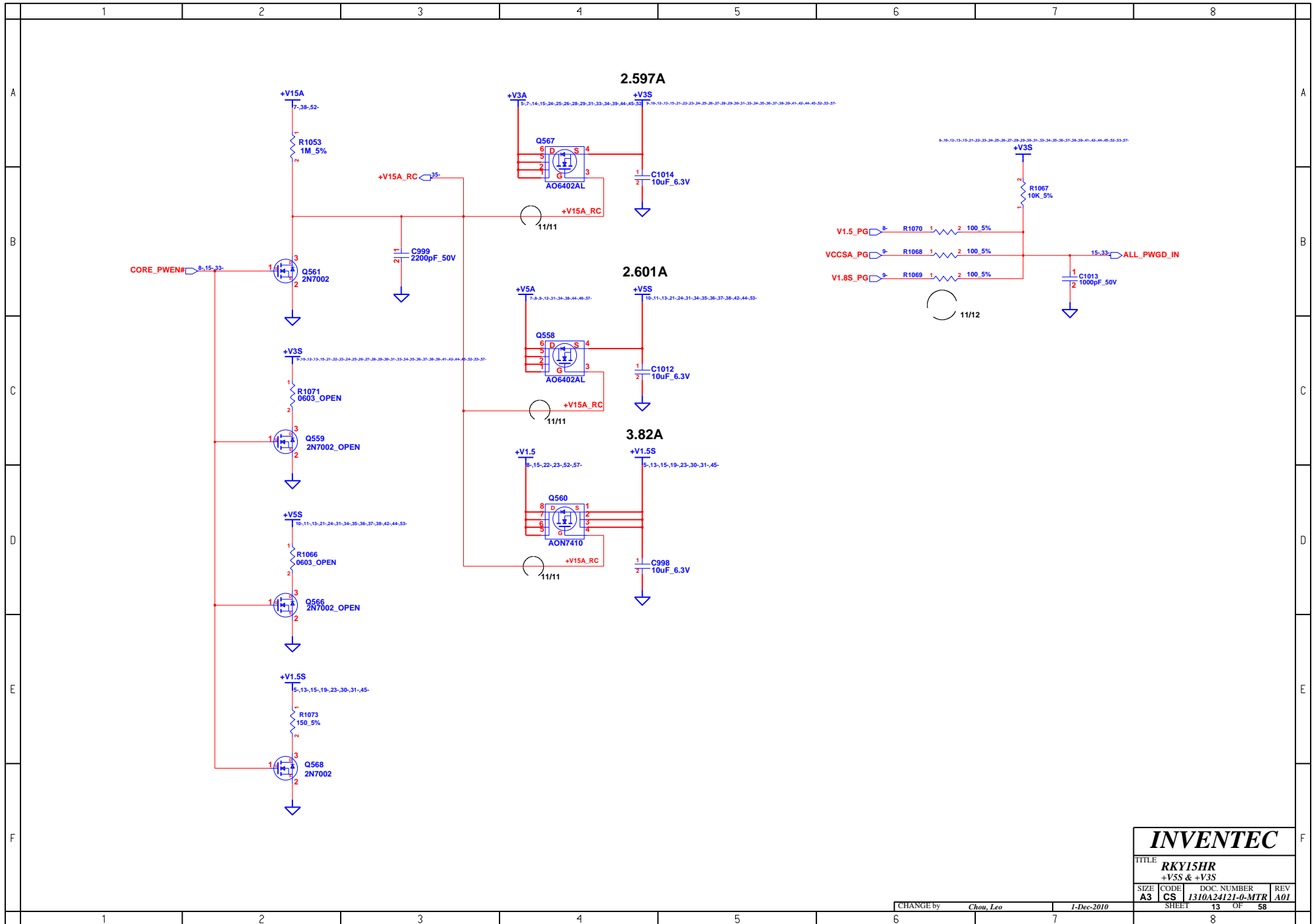


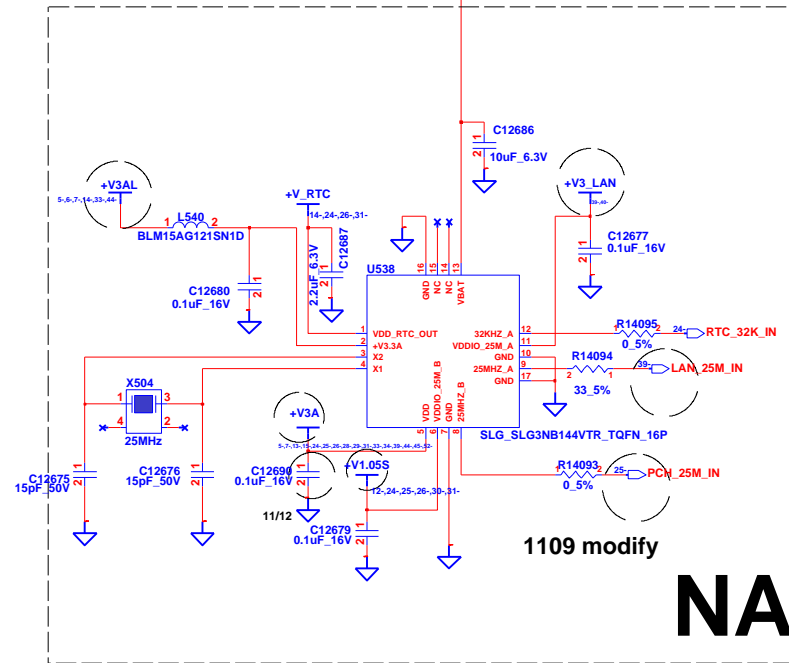
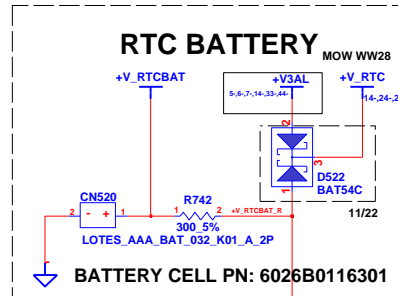




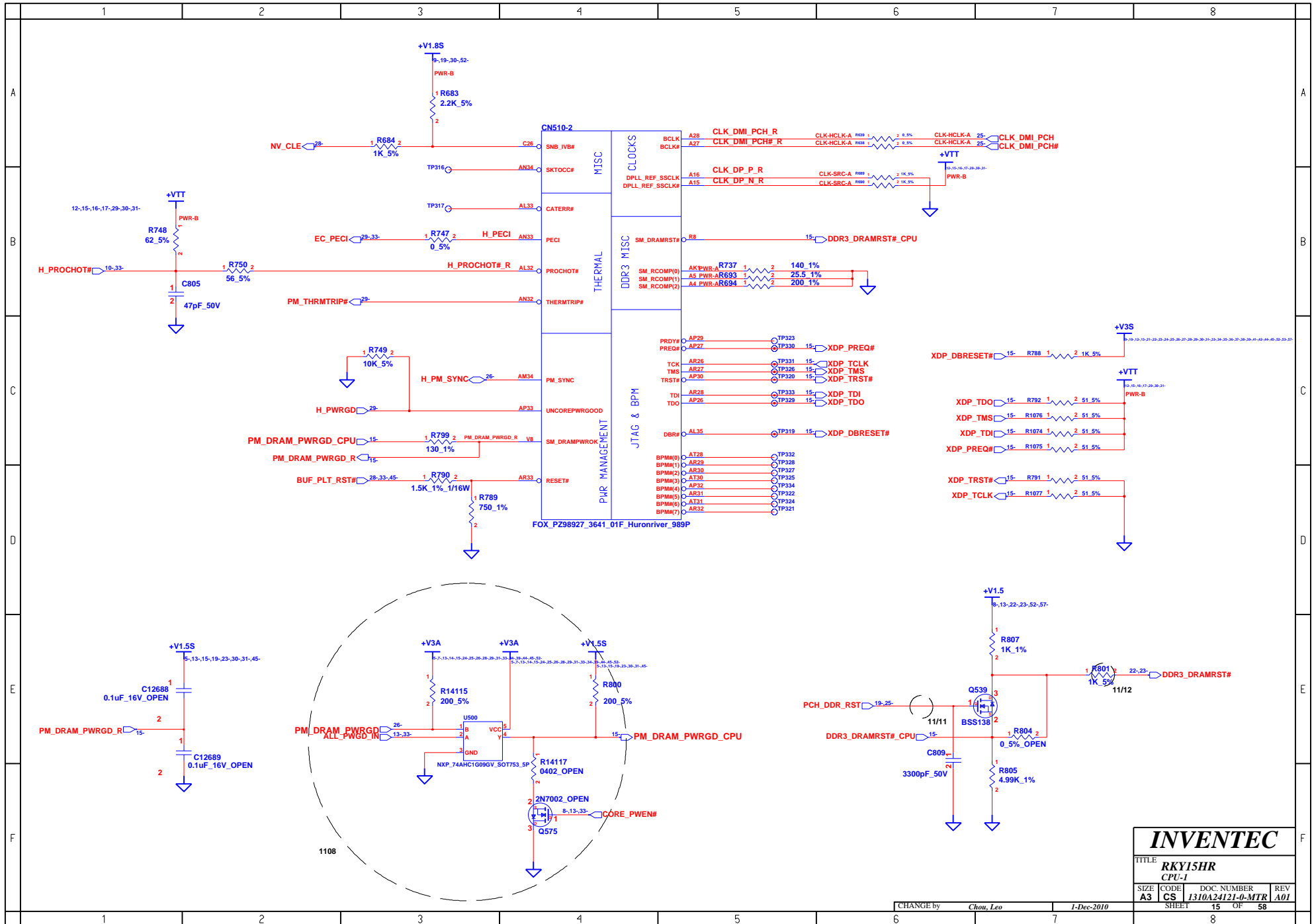


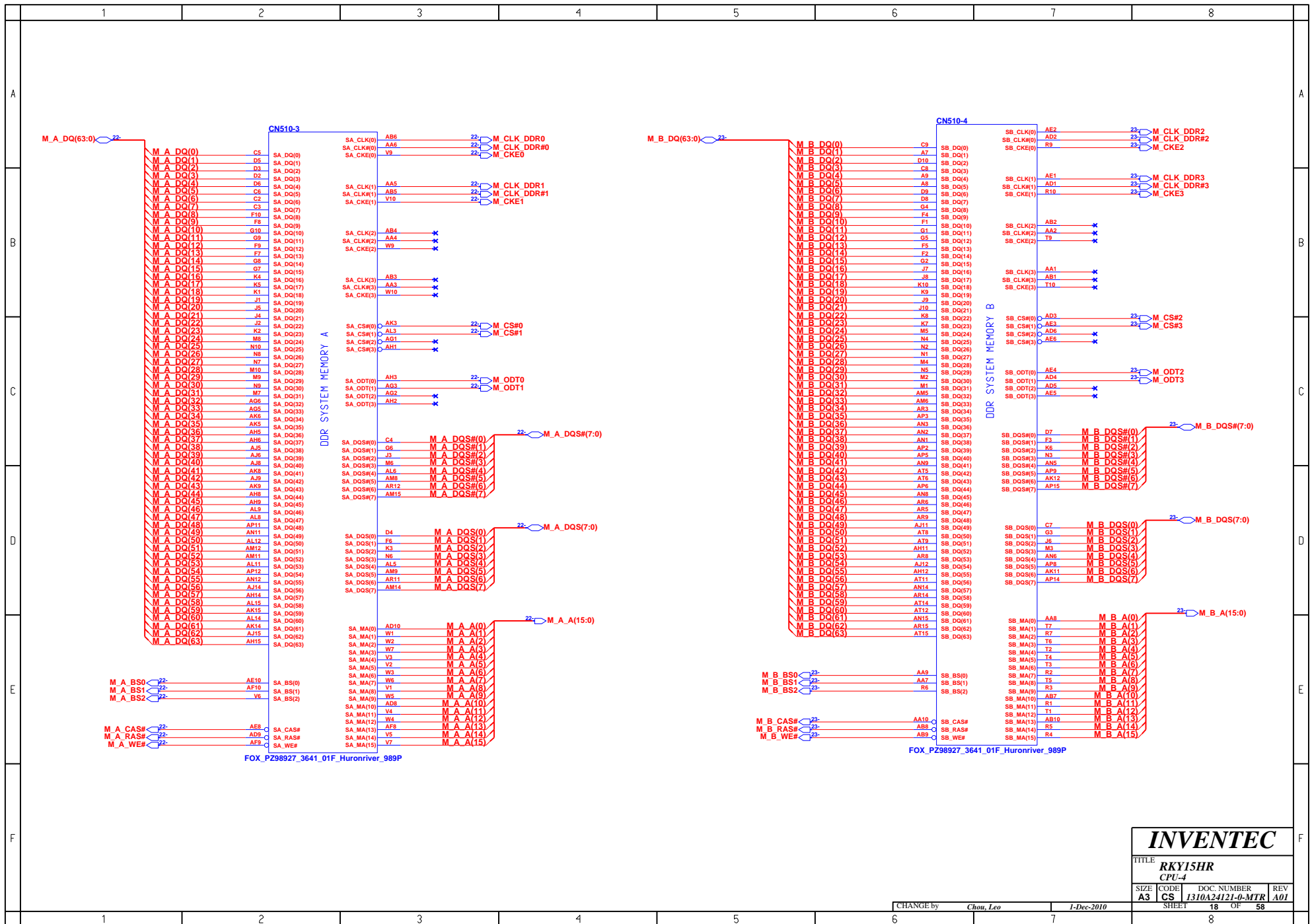


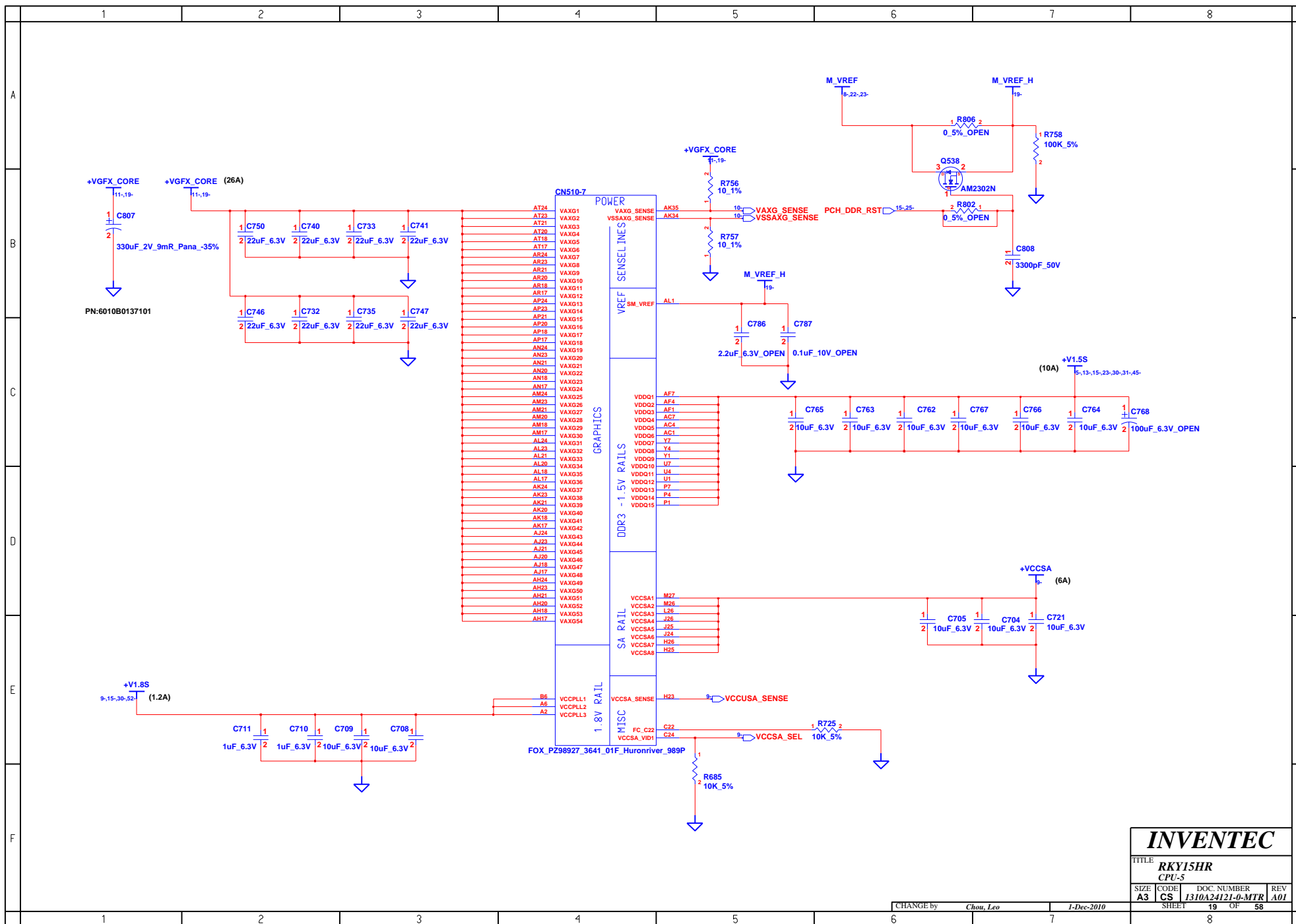


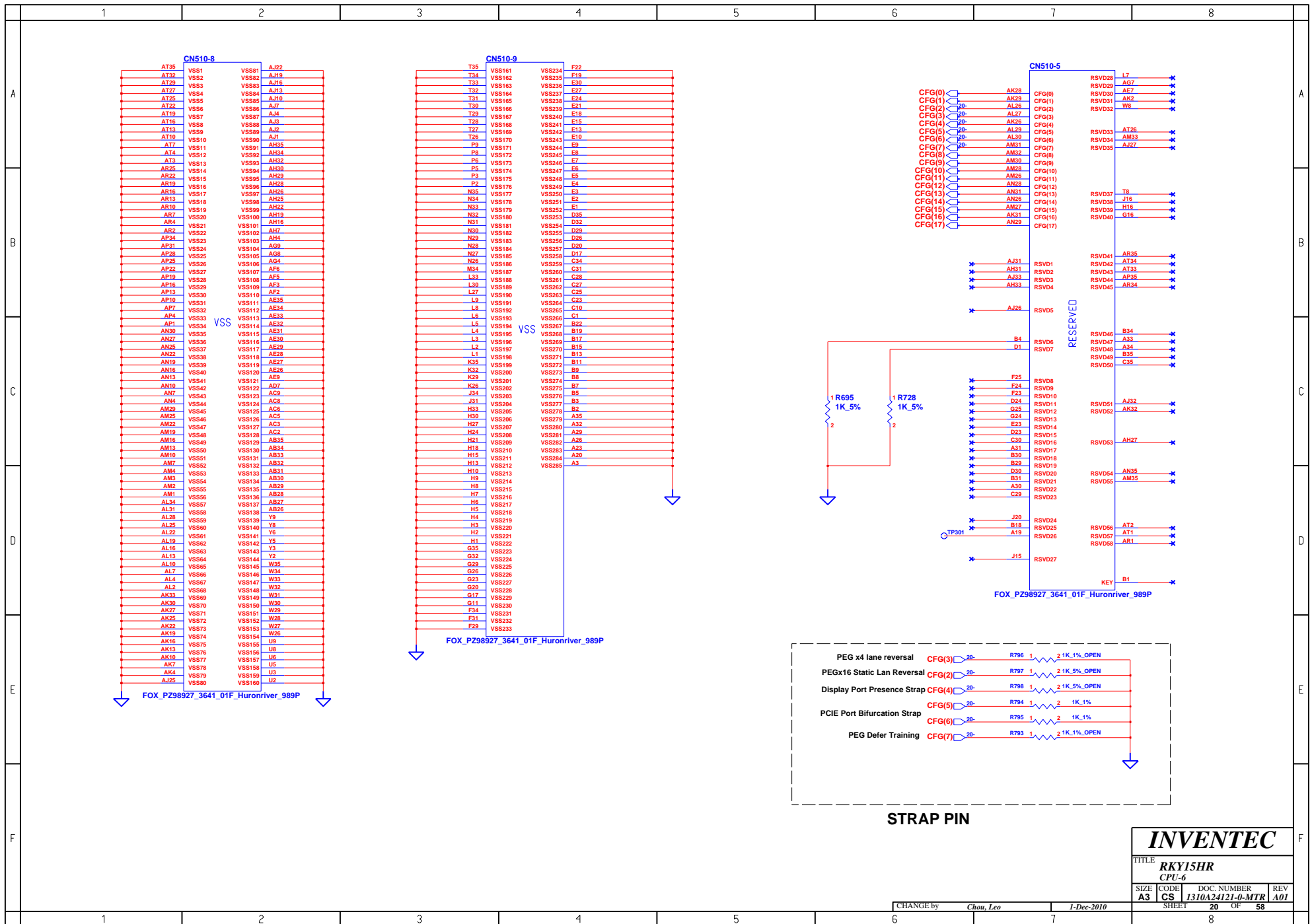


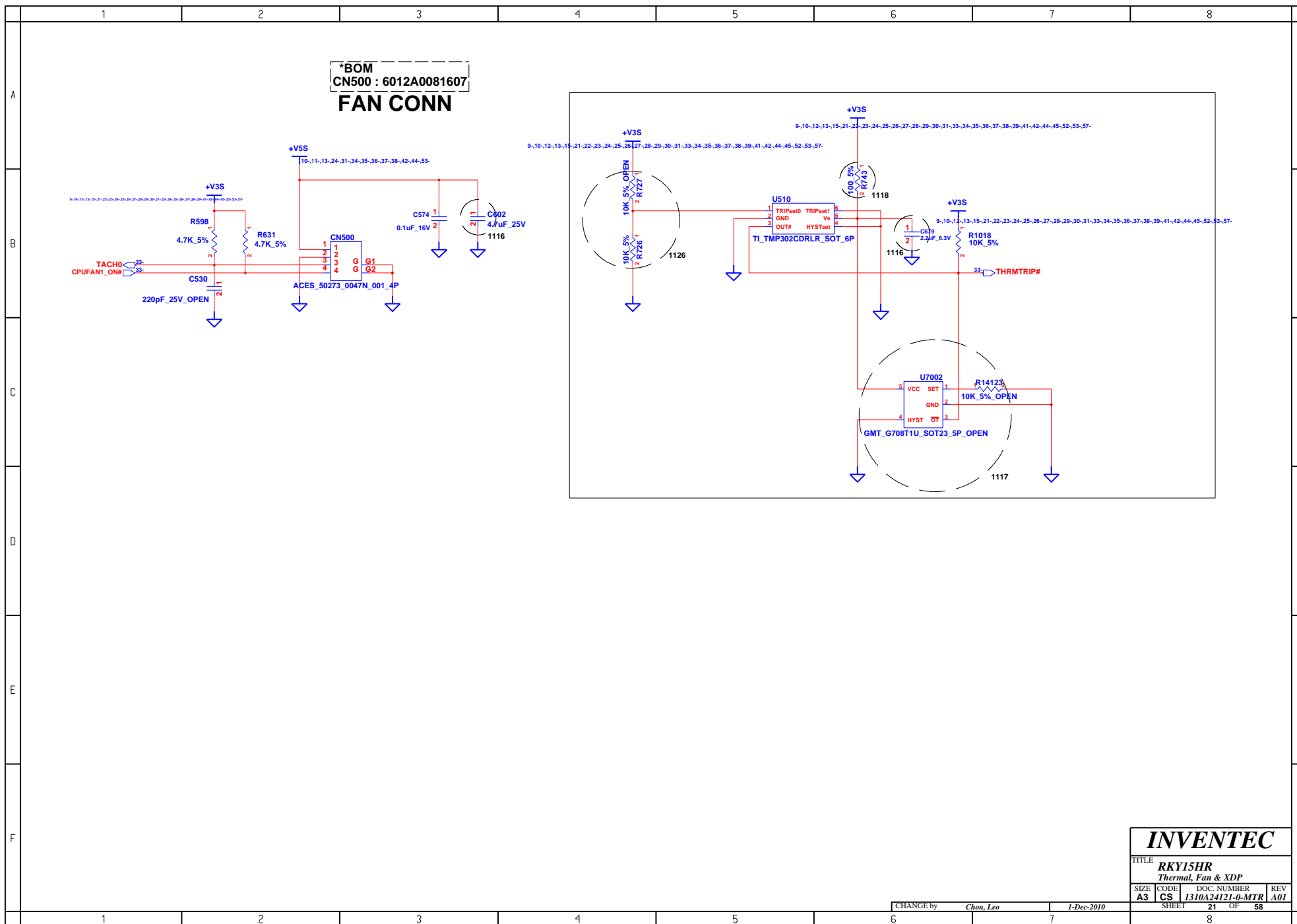
INVENTEC			
TITLE RKY15HR RTC BATTERY			
SIZE A3	CODE CS	DOC. NUMBER 1310A24121-0-MTR	REV A01
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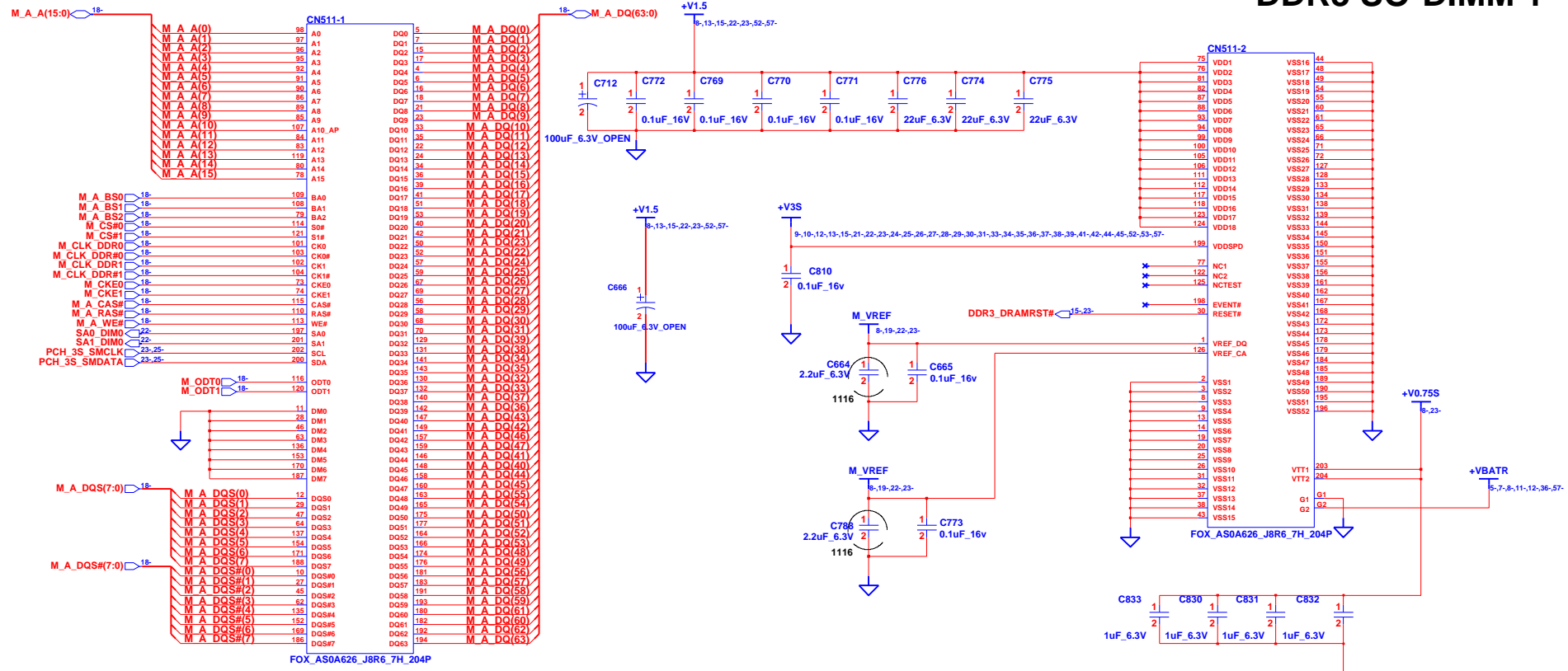








DDR3 SO-DIMM 1



NOTE:

IF SA0_DIM0=0, SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0xA0
SO-DIMMA TS ADDRESS IS 0x30

IF SA0_DIM0=1, SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0xA2
SO-DIMMA TS ADDRESS IS 0x32

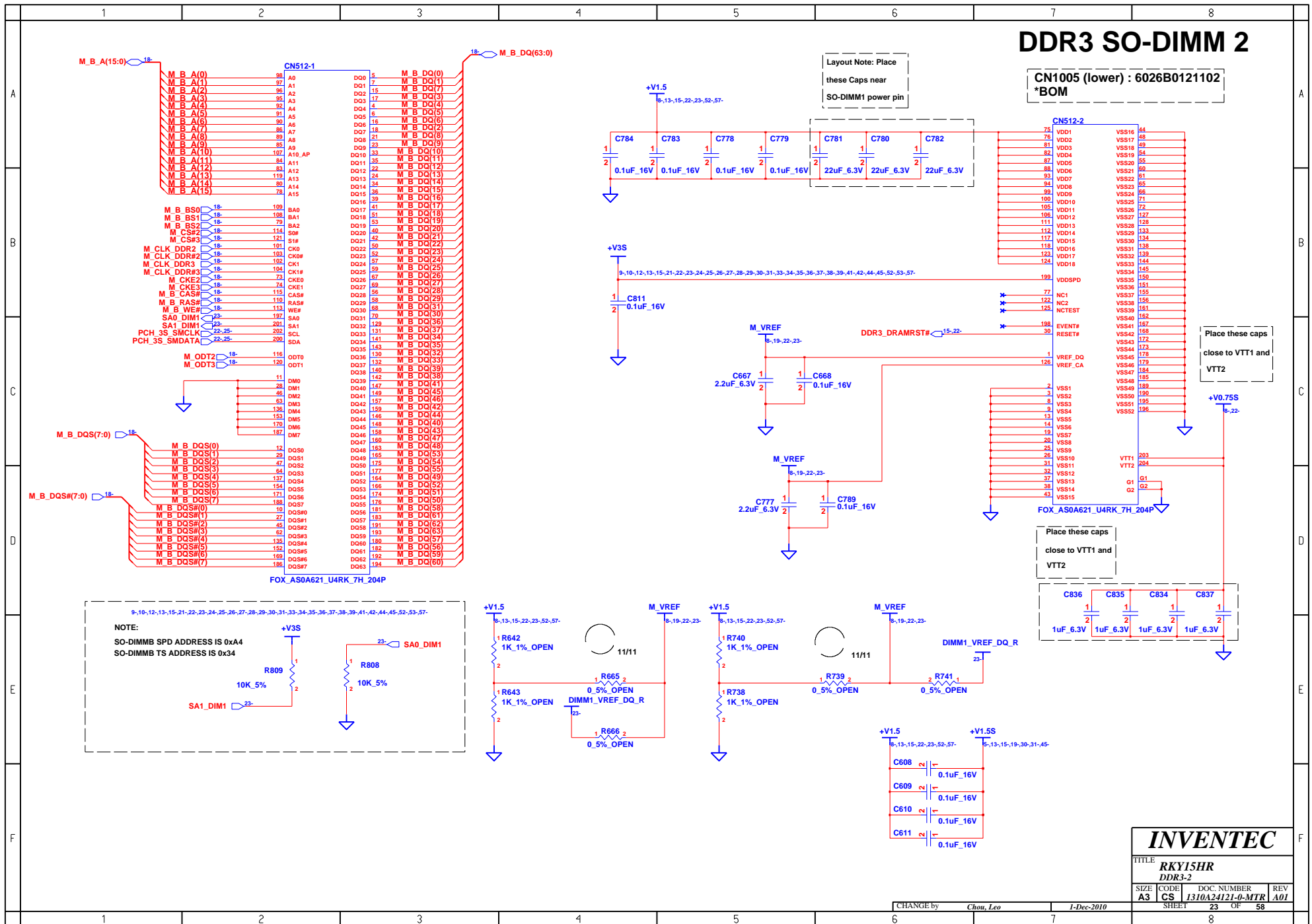
INVENTEC

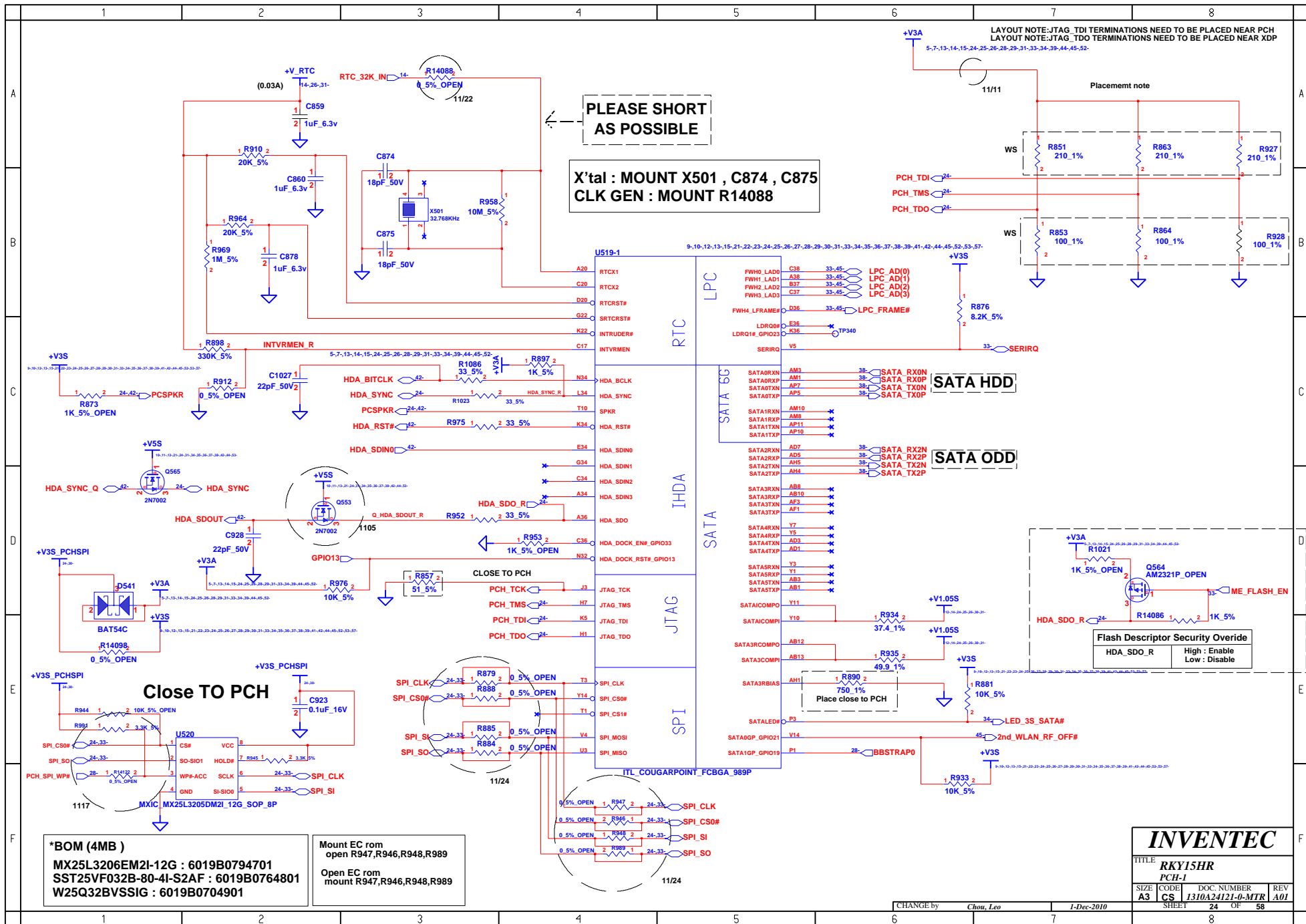
TITLE: **RKY15HR**
DDR3-1

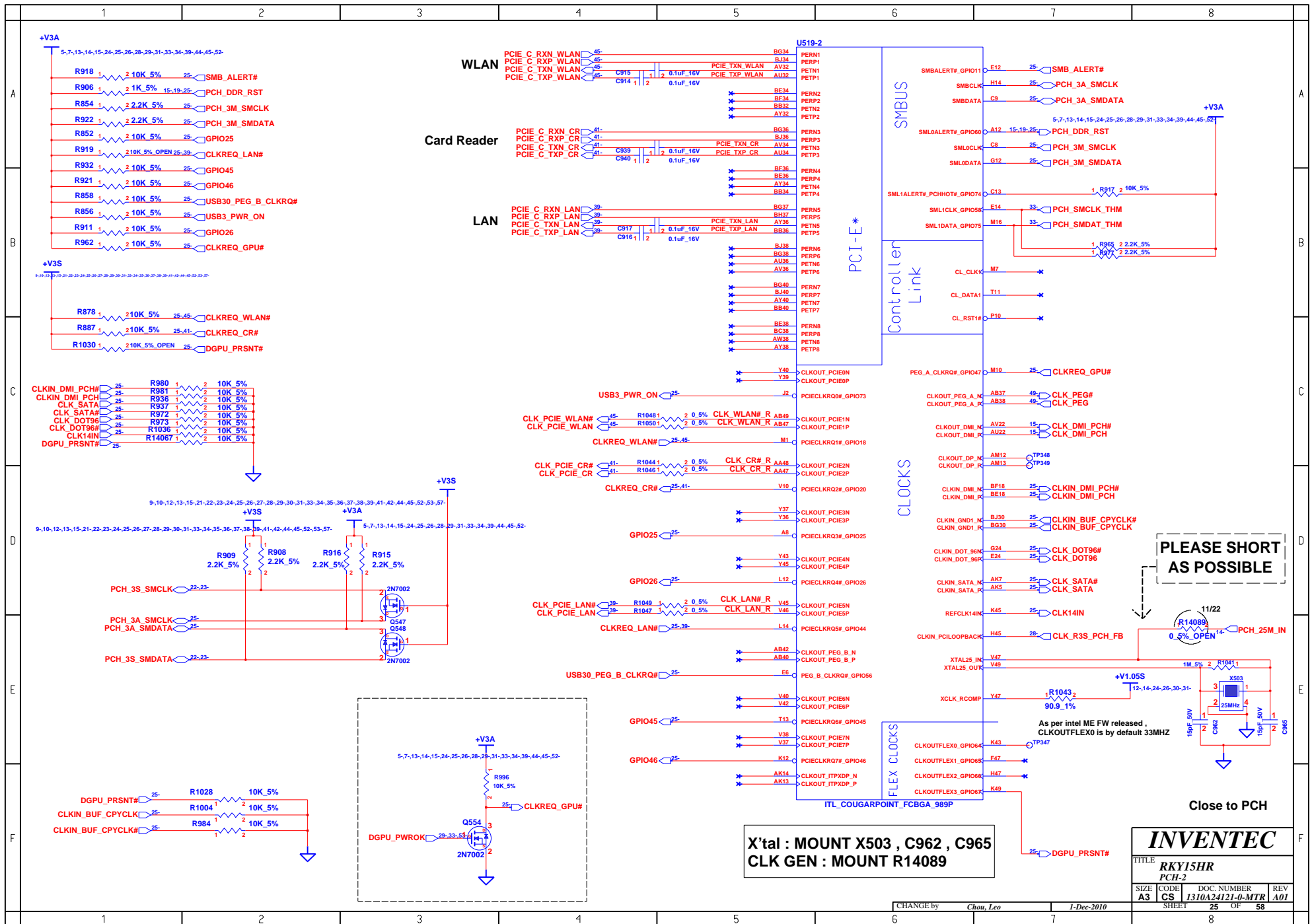
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A24121-0-MTR	A01

CHANGE by: *Chou, Leo* 1-Dec-2010

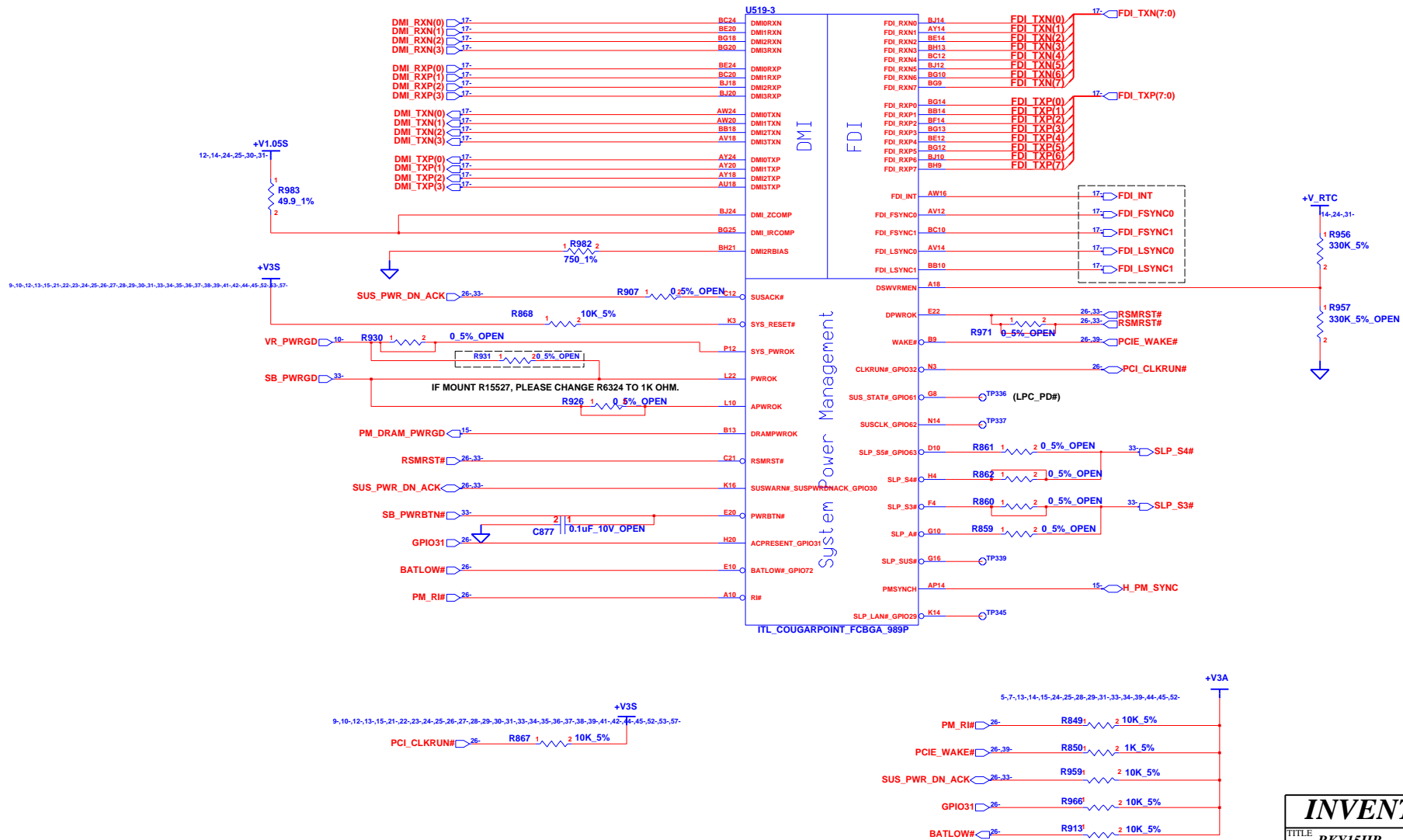
SHEET 22 OF 58







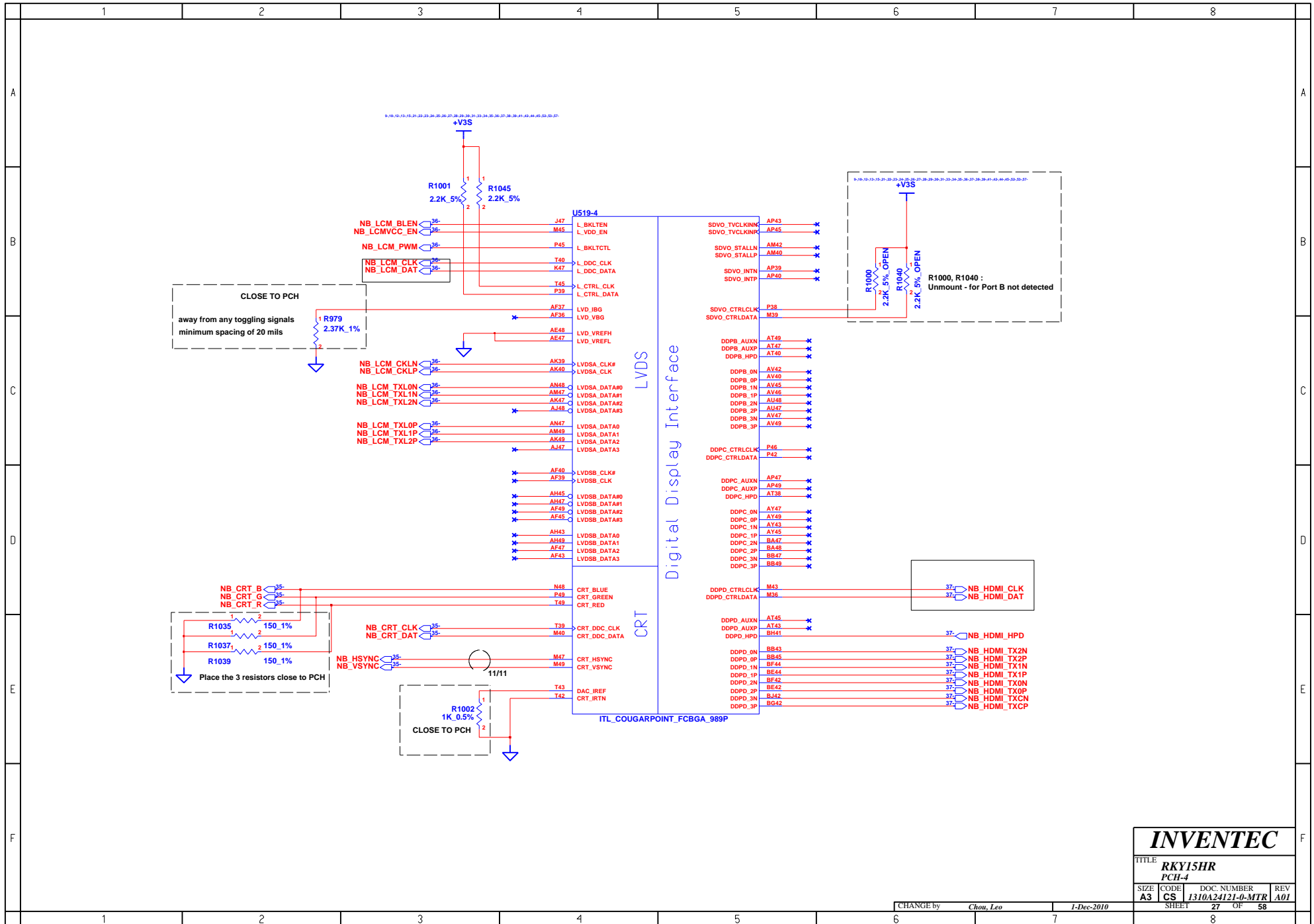
Note: 1.SLP_SUS and SUSACK# are NC if DSW is not supported
 2.DPWROK should connect to RSMRST# if DSW not supported
 3.PCH_DPWROK pull up to +V3S enables DSW support. No install R5261 to disable DSW

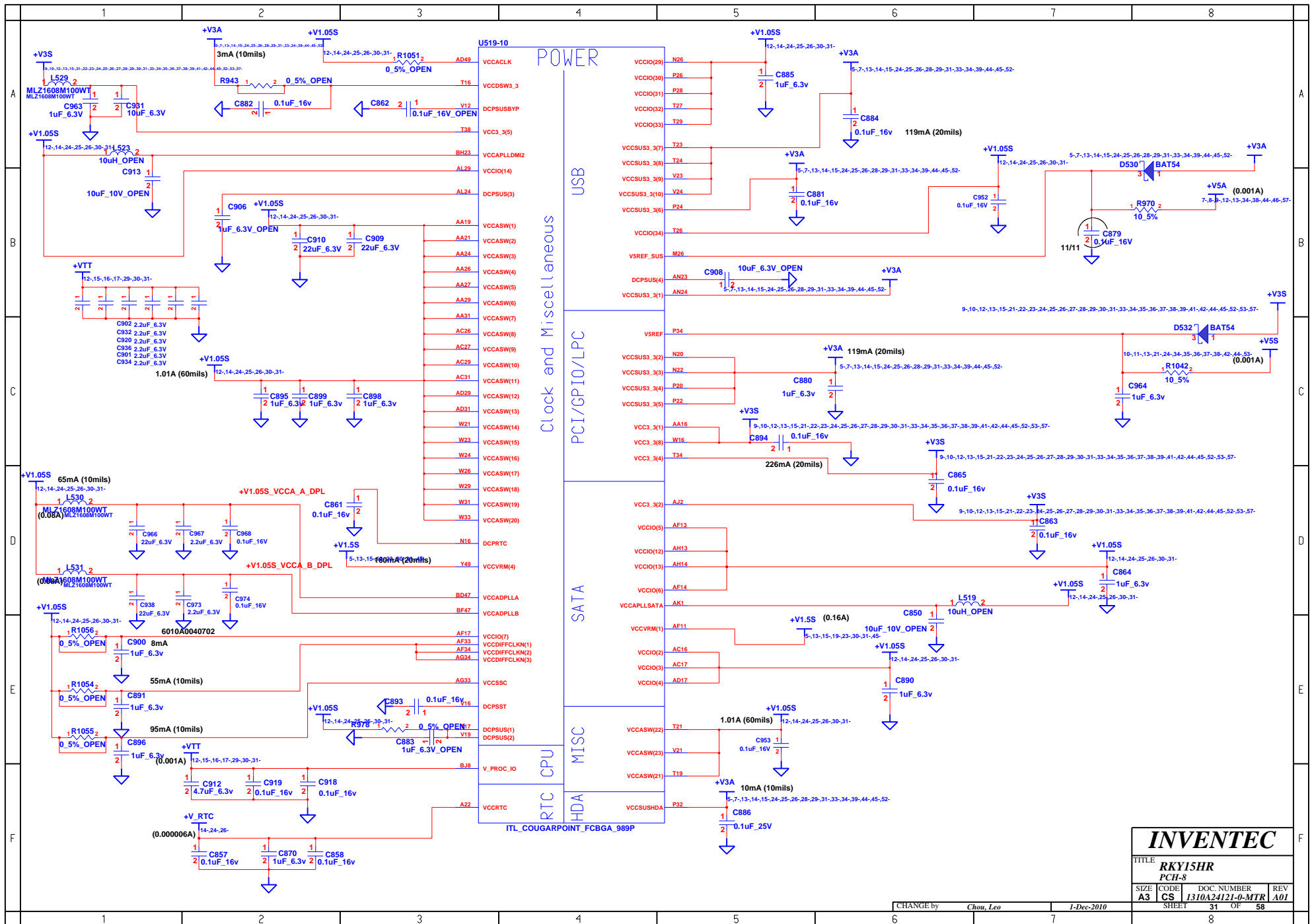


INVENTEC

TITLE RKY15HR PCH-3			
SIZE A3	CODE CS	DOC. NUMBER 1310A24121-0-MTR	REV A01
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U519-8

HS VSS(0)

AA17 VSS(1) VSS(80) AK38

AA2 VSS(2) VSS(81) AK4

AA3 VSS(3) VSS(82) AK2

AA33 VSS(4) VSS(83) AK46

AA34 VSS(5) VSS(84) AK8

AB11 VSS(6) VSS(85) AL16

AB14 VSS(7) VSS(86) AL17

AB39 VSS(8) VSS(87) AL19

AB4 VSS(9) VSS(88) AL2

AB43 VSS(10) VSS(89) AL21

AB5 VSS(11) VSS(90) AL23

AB7 VSS(12) VSS(91) AL26

AC19 VSS(13) VSS(92) AL27

AC2 VSS(14) VSS(93) AL31

AC21 VSS(15) VSS(94) AL33

AC24 VSS(16) VSS(95) AL34

AC33 VSS(17) VSS(96) AL48

AC34 VSS(18) VSS(97) AM11

AC45 VSS(19) VSS(98) AM14

AD16 VSS(20) VSS(99) AM36

AD11 VSS(21) VSS(100) AM39

AD12 VSS(22) VSS(101) AM43

AD13 VSS(23) VSS(102) AM45

AD19 VSS(24) VSS(103) AM46

AD24 VSS(25) VSS(104) AM7

AD26 VSS(26) VSS(105) AN2

AD27 VSS(27) VSS(106) AN29

AD33 VSS(28) VSS(107) AN3

AD34 VSS(29) VSS(108) AN31

AD36 VSS(30) VSS(109) AP12

AD37 VSS(31) VSS(110) AP19

AD38 VSS(32) VSS(111) AP28

AD39 VSS(33) VSS(112) AP30

AD4 VSS(34) VSS(113) AP32

AD40 VSS(35) VSS(114) AP38

AD42 VSS(36) VSS(115) AP4

AD43 VSS(37) VSS(116) AP42

AD45 VSS(38) VSS(117) AP46

AD46 VSS(39) VSS(118) AP8

AD5 VSS(40) VSS(119) AR2

AE1 VSS(41) VSS(120) AR46

AE3 VSS(42) VSS(121) AT11

AF10 VSS(43) VSS(122) AT13

AF12 VSS(44) VSS(123) AT18

AF14 VSS(45) VSS(124) AT22

AF16 VSS(46) VSS(125) AT26

AF19 VSS(47) VSS(126) AT28

AF24 VSS(48) VSS(127) AT30

AF26 VSS(49) VSS(128) AT32

AF27 VSS(50) VSS(129) AT34

AF27 VSS(51) VSS(130) AT39

AF29 VSS(52) VSS(131) AT42

AF31 VSS(53) VSS(132) AT46

AF38 VSS(54) VSS(133) AT7

AF4 VSS(55) VSS(134) AU24

AF41 VSS(56) VSS(135) AU30

AF46 VSS(57) VSS(136) AV16

AF5 VSS(58) VSS(137) AV20

AF7 VSS(59) VSS(138) AV24

AF9 VSS(60) VSS(139) AV30

AG19 VSS(61) VSS(140) AV38

AG2 VSS(62) VSS(141) AV4

AG31 VSS(63) VSS(142) AV43

AG48 VSS(64) VSS(143) AV8

AH11 VSS(65) VSS(144) AW14

AH3 VSS(66) VSS(145) AW18

AH36 VSS(67) VSS(146) AW2

AH39 VSS(68) VSS(147) AW22

AH40 VSS(69) VSS(148) AW26

AH42 VSS(70) VSS(149) AW28

AH46 VSS(71) VSS(150) AW32

AH7 VSS(72) VSS(151) AW34

AJ19 VSS(73) VSS(152) AW36

AJ21 VSS(74) VSS(153) AW40

AJ24 VSS(75) VSS(154) AW48

AJ33 VSS(76) VSS(155) AV11

AJ34 VSS(77) VSS(156) AY12

AK12 VSS(78) VSS(157) AY22

AK3 VSS(79) VSS(158) AY28

ITL_COUGARPOINT_FCBGA_989P

U519-9

AY4 VSS(159) VSS(259) H46

AY42 VSS(160) VSS(260) K18

AY46 VSS(161) VSS(261) K26

AY8 VSS(162) VSS(262) K39

B11 VSS(163) VSS(263) K46

B15 VSS(164) VSS(264) K7

B19 VSS(165) VSS(265) L18

B23 VSS(166) VSS(266) L2

B27 VSS(167) VSS(267) L26

B35 VSS(168) VSS(268) L28

B39 VSS(169) VSS(269) L36

B7 VSS(171) VSS(271) L48

F45 VSS(172) VSS(272) M12

BB12 VSS(173) VSS(273) P16

BB16 VSS(174) VSS(274) M18

BB20 VSS(175) VSS(275) M22

BB22 VSS(176) VSS(276) M24

BB24 VSS(177) VSS(277) M30

BB28 VSS(178) VSS(278) M32

BB30 VSS(179) VSS(279) M34

BB36 VSS(180) VSS(280) M38

BB4 VSS(181) VSS(281) M4

BB46 VSS(182) VSS(282) M42

BC14 VSS(183) VSS(283) M46

BC18 VSS(184) VSS(284) M8

BC2 VSS(185) VSS(285) N18

BC22 VSS(186) VSS(286) P30

BC26 VSS(187) VSS(287) N47

BC32 VSS(188) VSS(288) P11

BC34 VSS(189) VSS(289) P18

BC36 VSS(190) VSS(290) P33

BC40 VSS(191) VSS(291) P40

BC42 VSS(192) VSS(292) P43

BC48 VSS(193) VSS(293) P47

BD46 VSS(194) VSS(294) R2

BD5 VSS(195) VSS(295) R48

BE22 VSS(196) VSS(296) T21

BE26 VSS(197) VSS(297) T37

BE40 VSS(198) VSS(298) T4

BF10 VSS(199) VSS(299) W3

BF12 VSS(200) VSS(300) W4

BF16 VSS(201) VSS(301) W4

BF20 VSS(202) VSS(302) T46

BF22 VSS(203) VSS(303) T47

BF24 VSS(204) VSS(304) T8

BF26 VSS(205) VSS(305) V11

BF28 VSS(206) VSS(306) V17

BD3 VSS(207) VSS(307) V26

BF30 VSS(208) VSS(308) V27

BF38 VSS(209) VSS(309) V29

BF40 VSS(210) VSS(310) V31

BF8 VSS(211) VSS(311) V36

BG17 VSS(212) VSS(312) V39

BG21 VSS(213) VSS(313) V43

BG33 VSS(214) VSS(314) V7

BG44 VSS(215) VSS(315) W17

BG8 VSS(216) VSS(316) W19

BH11 VSS(217) VSS(317) W2

BH15 VSS(218) VSS(318) W27

BH17 VSS(219) VSS(319) W48

BH19 VSS(220) VSS(320) Y12

H19 VSS(221) VSS(321) Y38

BH27 VSS(222) VSS(322) Y4

BH31 VSS(223) VSS(323) Y42

BH33 VSS(224) VSS(324) Y46

BH35 VSS(225) VSS(325) Y2

BH39 VSS(226) VSS(326) BG29

BH43 VSS(227) VSS(327) N24

BH7 VSS(228) VSS(328) A3

D3 VSS(229) VSS(329) AD47

D12 VSS(230) VSS(330) B43

D16 VSS(231) VSS(331) BE10

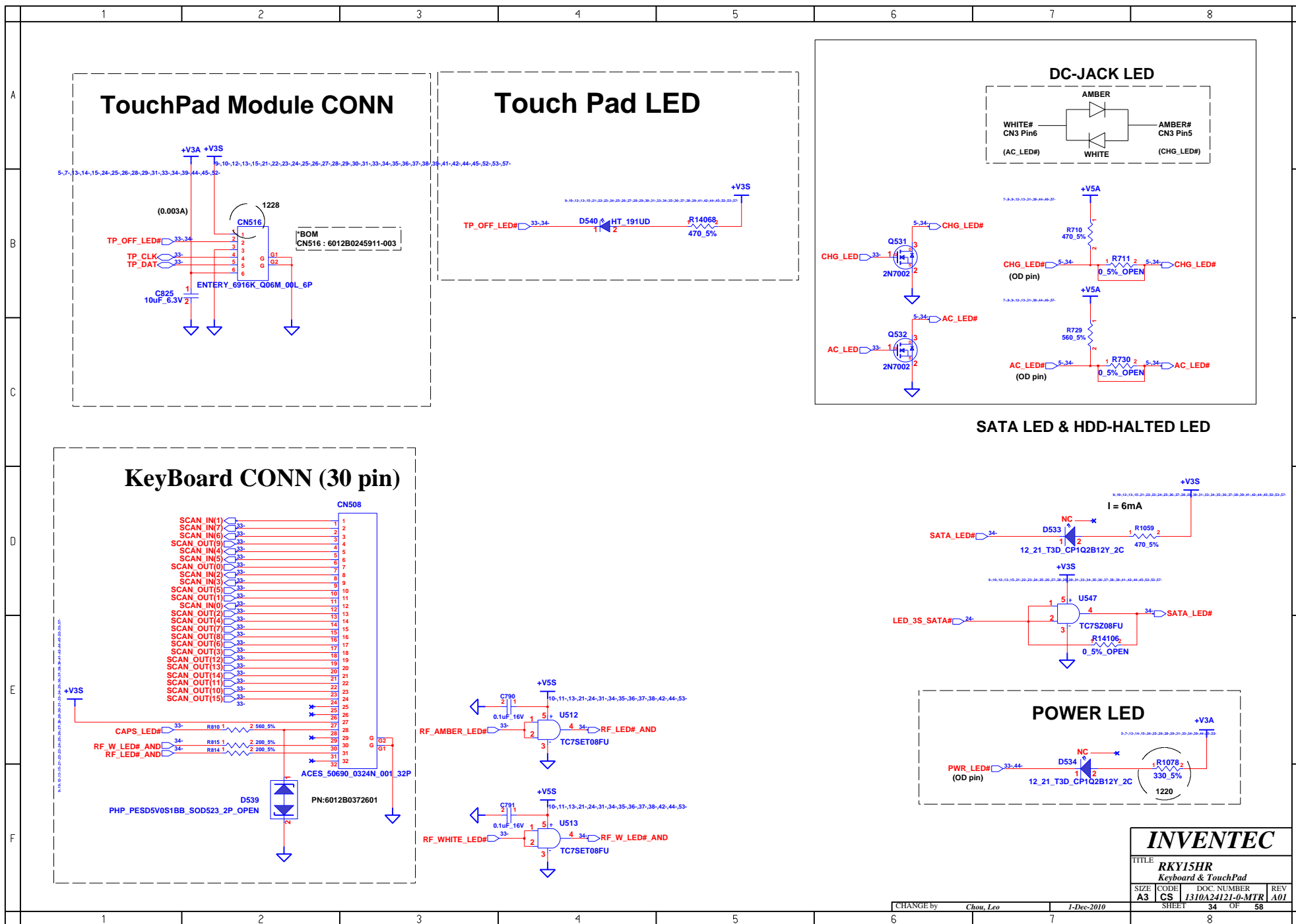
D18 VSS(232) VSS(332) BG41

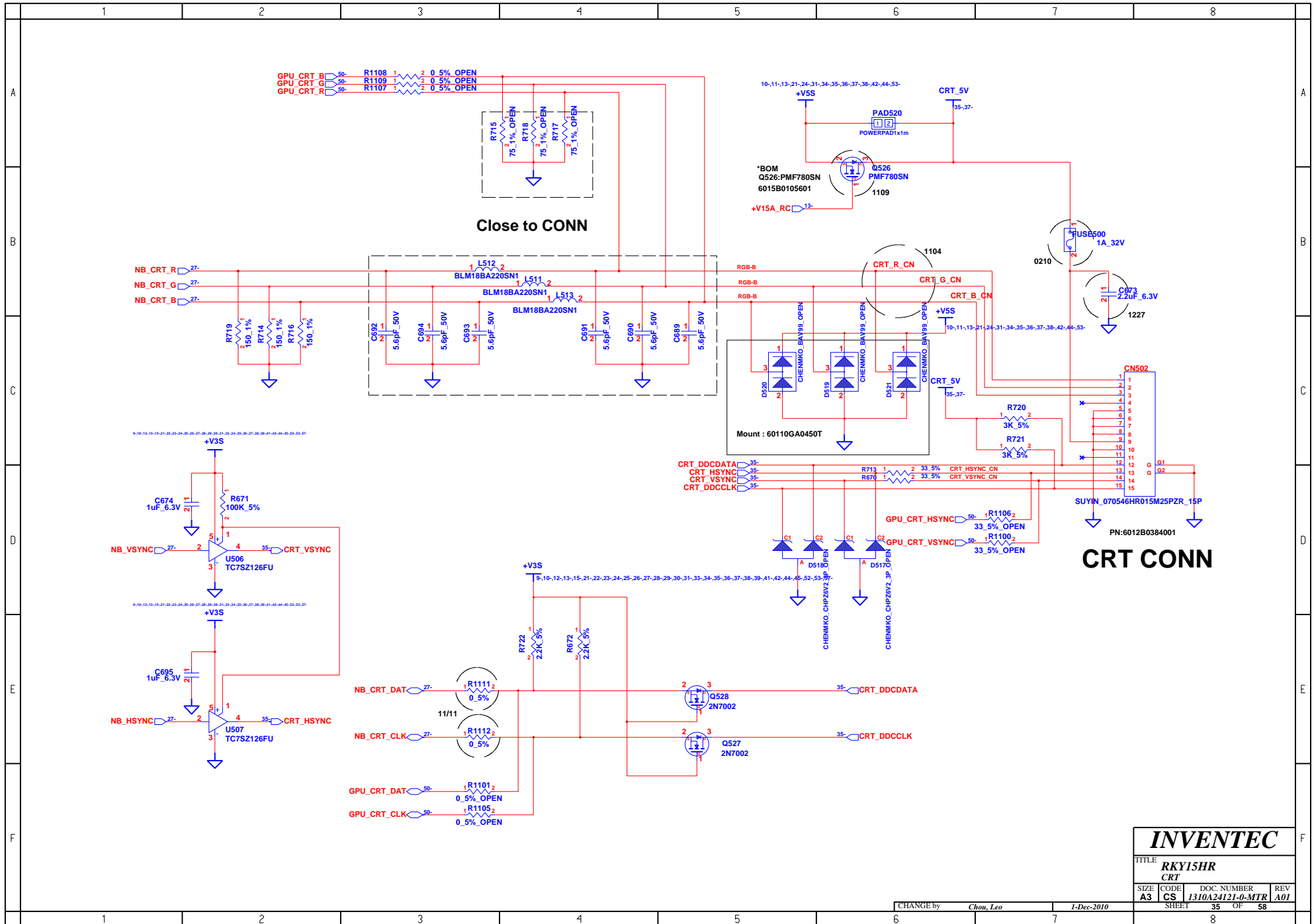
D22 VSS(233) VSS(333) G14

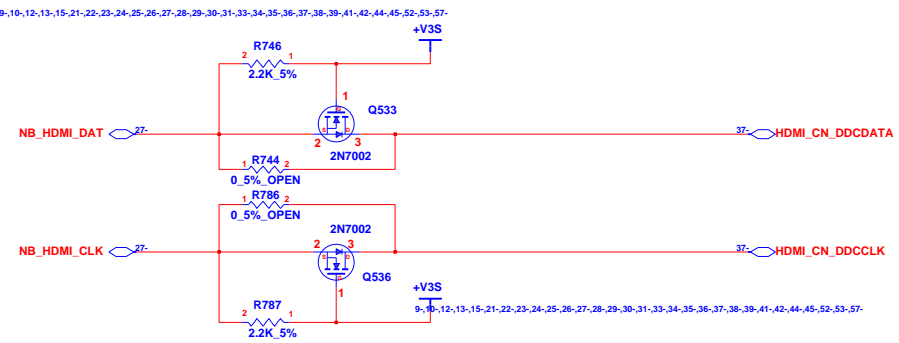
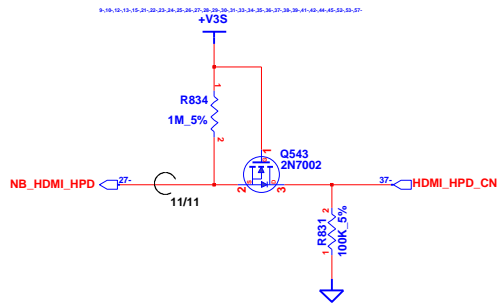
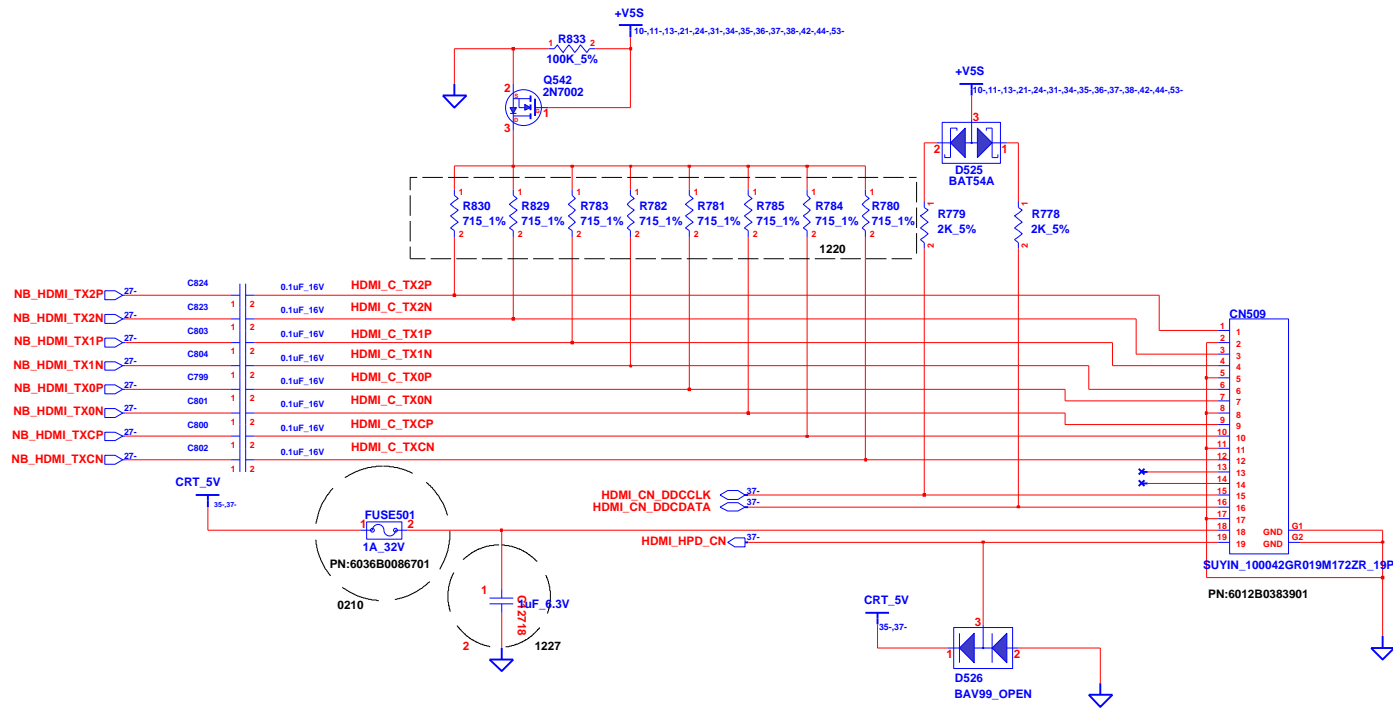
D24 VSS(234) VSS(334) H16

D26 VSS(235) VSS(335) T36

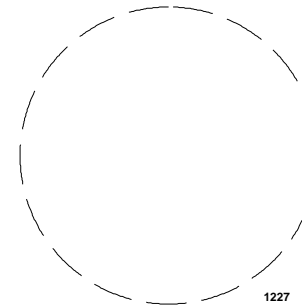
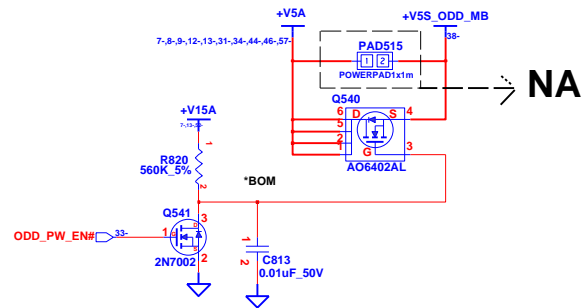
D30 VSS(23



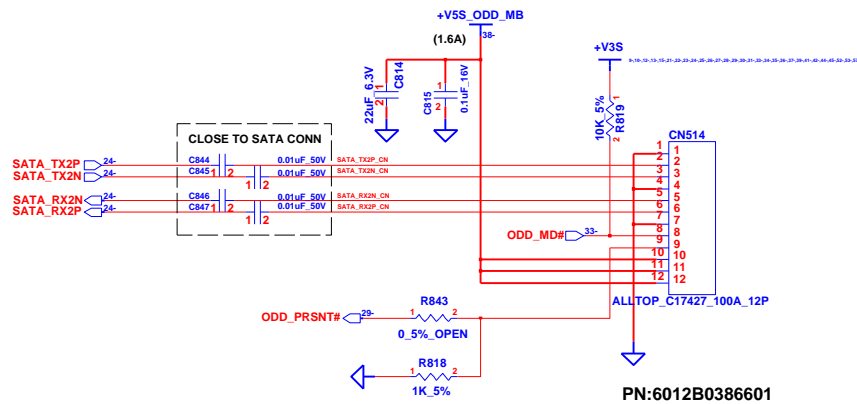




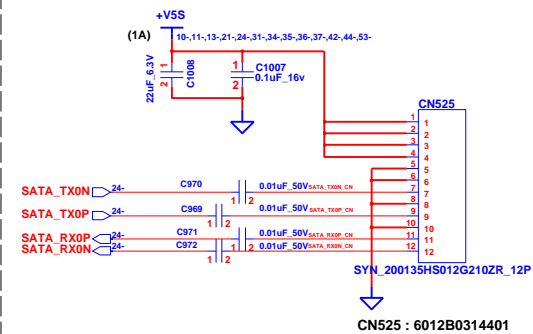
INVENTEC			
TITLE			
RKY15HR			
HDMI Level-Shift			
SIZE	CODE	DOC. NUMBER	REV
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SATA ODD CONN on MB



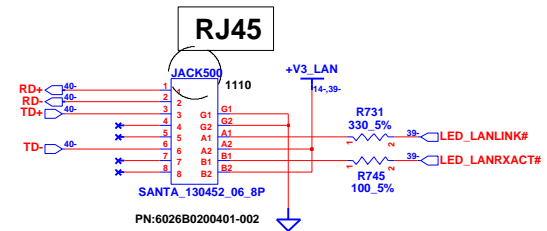
SATA HDD FPC CONN on MB



INVENTEC

TITLE RKY15HR SATA HDD & ODD			
SIZE A3	CODE CS	DOC. NUMBER 1310A24121-0-MTR	REV A01
SHEET 38		OF 58	

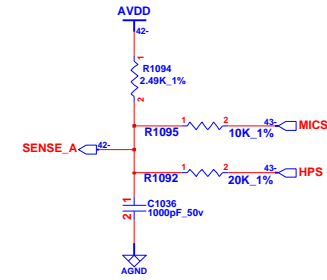
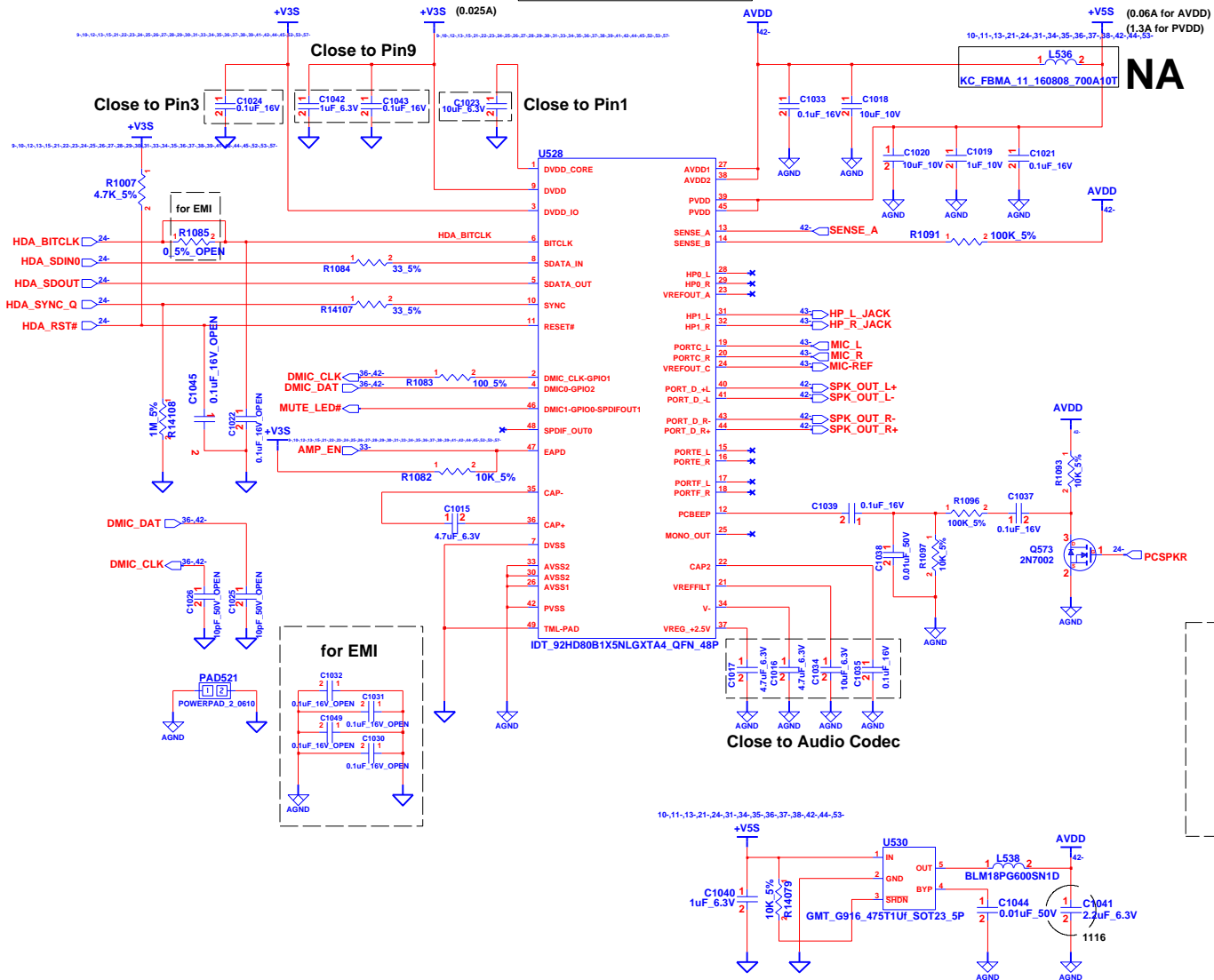
CHANGE by *Chou, Leo* 1-Dec-2010



INVENTEC			
TITLE RJ45			
SIZE A3	CODE CS	DOC. NUMBER 1310A24121-0-MTR	REV A01
SHEET 40		OF 58	

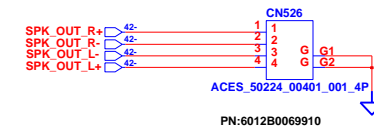
***BOM**

6019B0824101



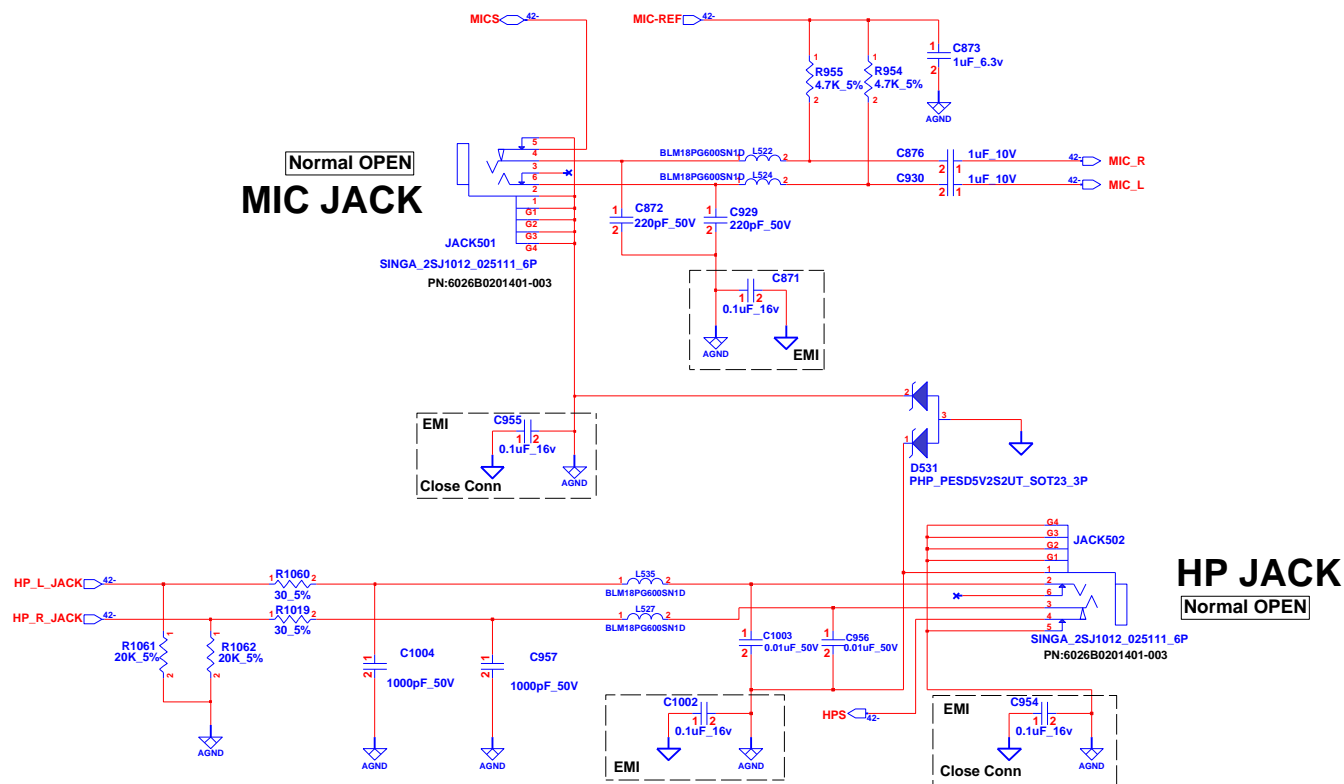
PORT	Discription	Sense
A	NO USE	
B	Headphone	HPS
C	Microphone	MICS
D	SPKR	
E	NO USE	
F	NO USE	

INT-SPEAKER CONN

**INVENTEC**

TITLE	<i>RKY15HR</i> <i>Audio Codec</i>
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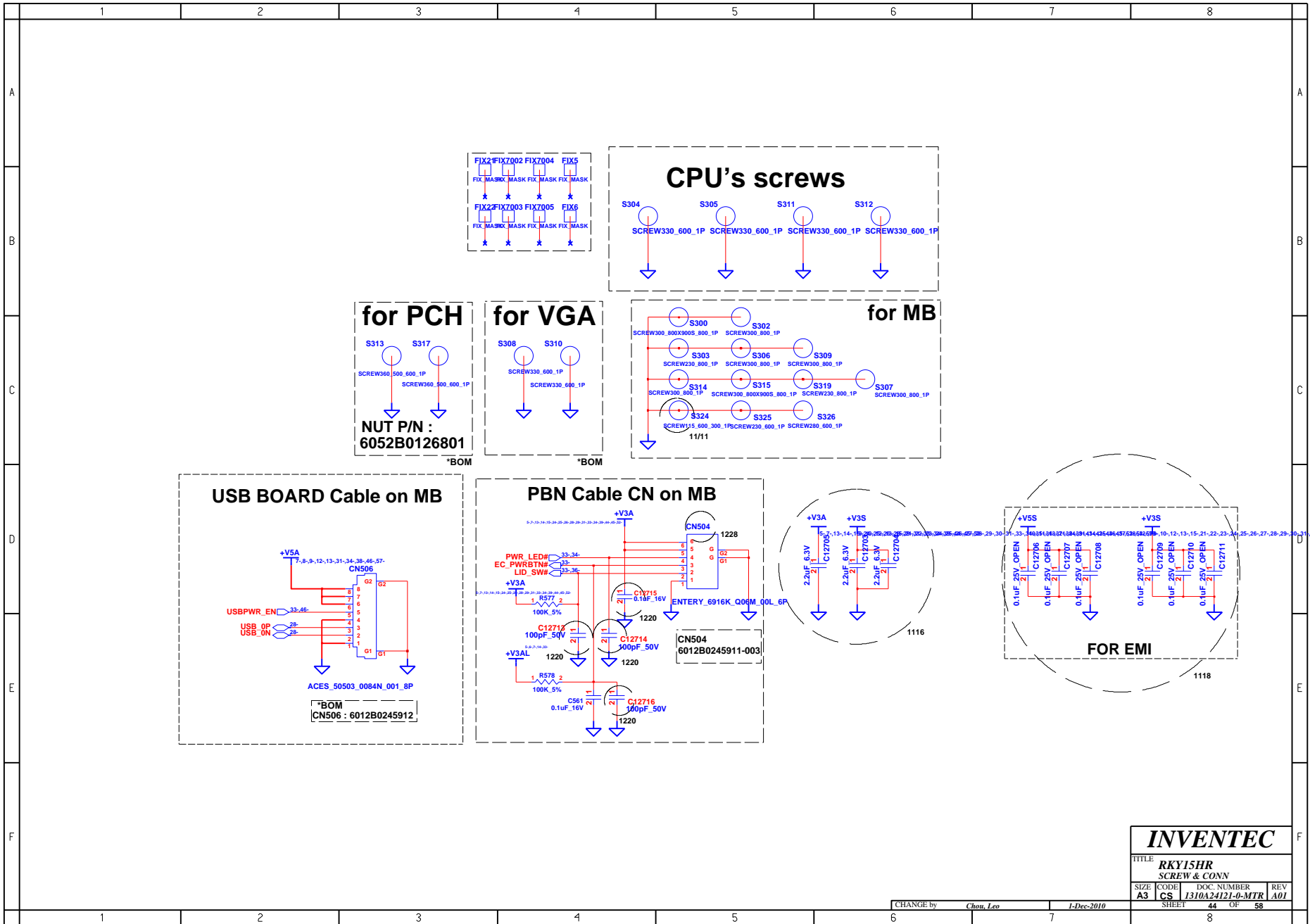
SIZE A3	CODE CS	DOC. NUMBER 1310A24121-0-MTR	REV A01
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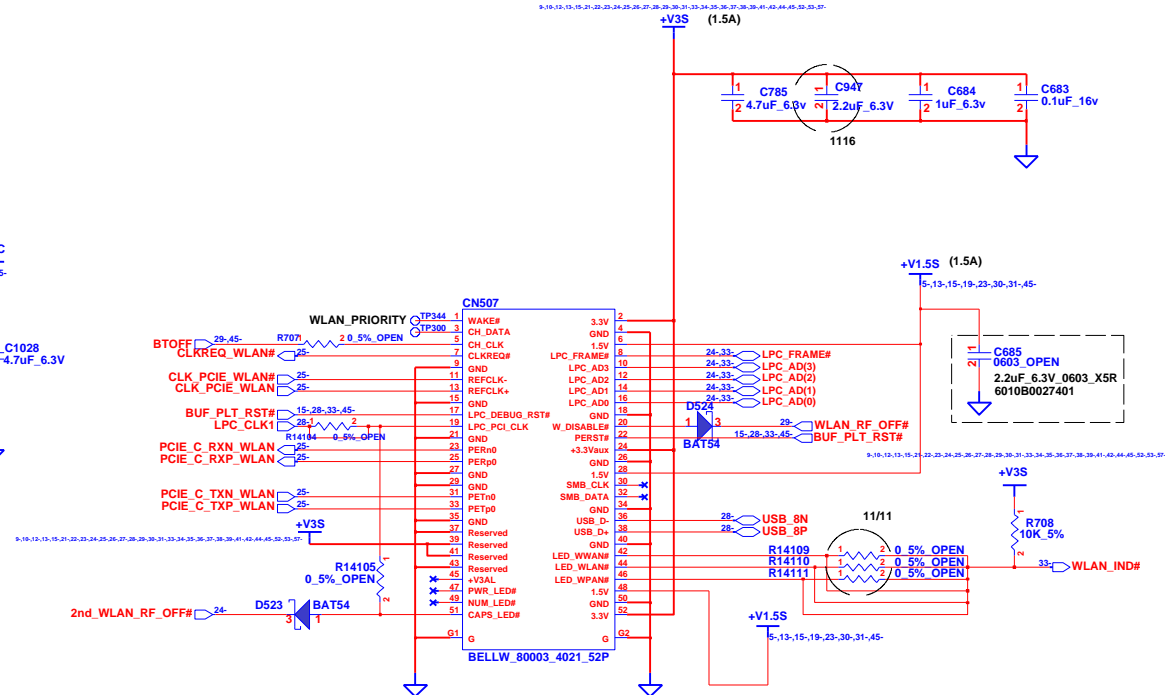
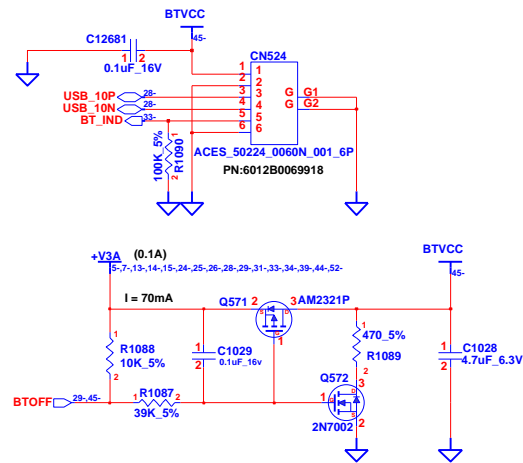
INVENTEC

TITLE			
RKY15HR			
HP JACK & MIC JACK			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A24121-0-MTR	A01
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WLAN CONN (MINICARD)

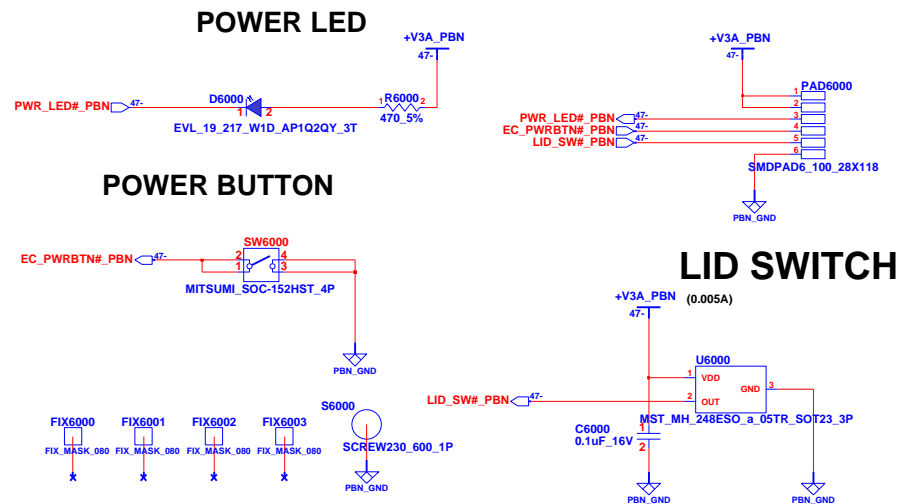


*BOM
CN507 : 6026B0140704

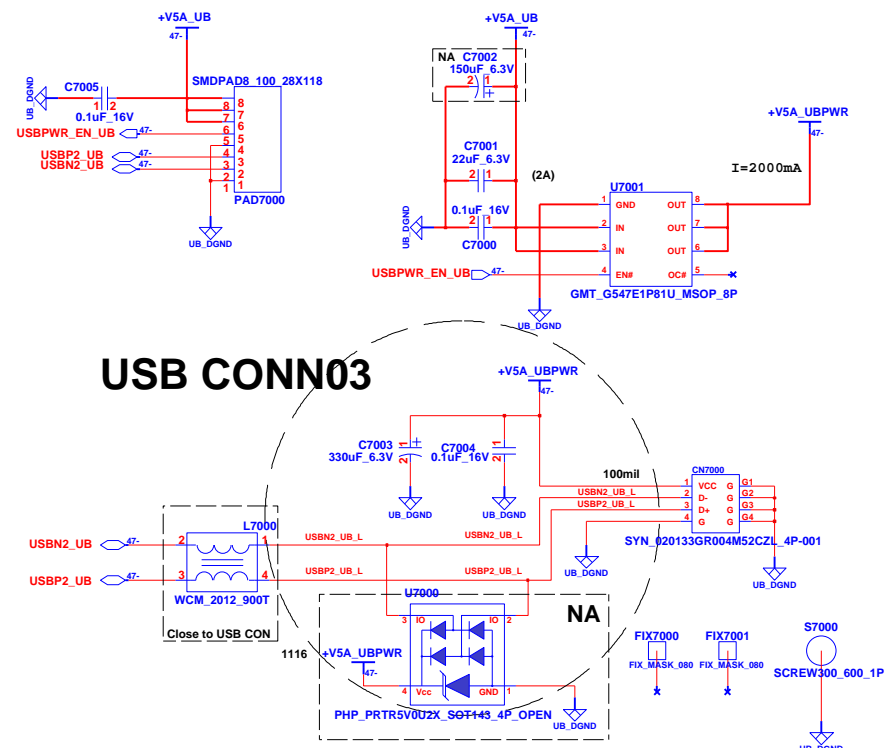
TITLE			
<i>RKY15HR</i> <i>WLAN, BT & WebCAM</i>			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A24121-0-MTR	A01
SHEET		45	OF 58

CHANGE by	<i>Chou, Leo</i>	<i>1-Dec-2010</i>
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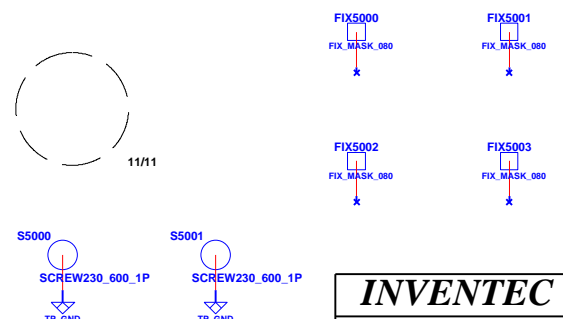
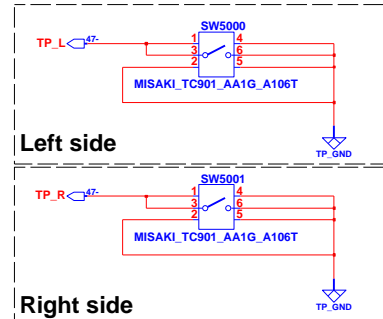
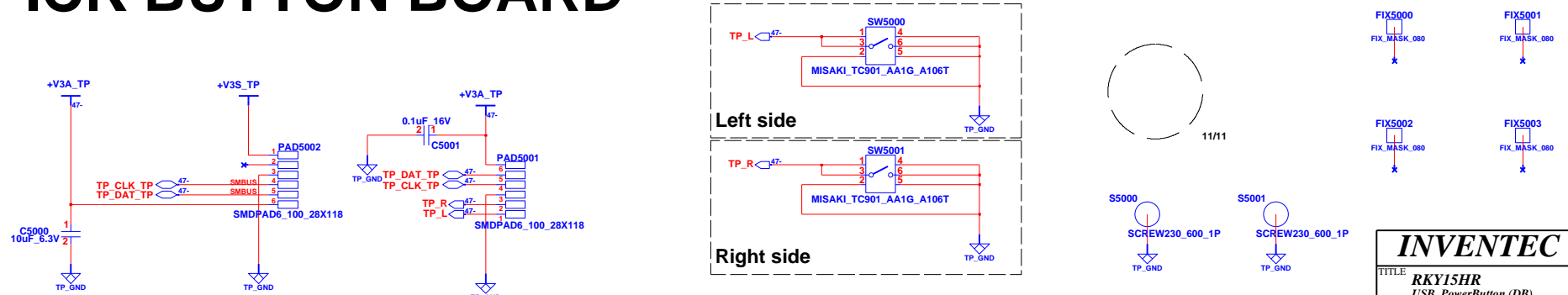
POWER BOTTON BOARD



USB BOARD



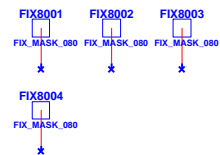
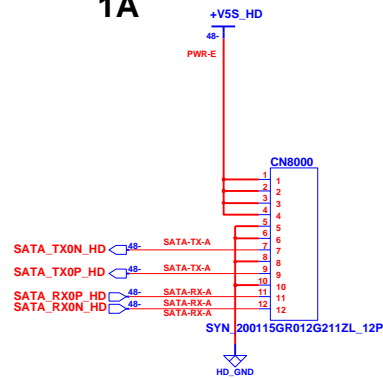
PICK BUTTON BOARD



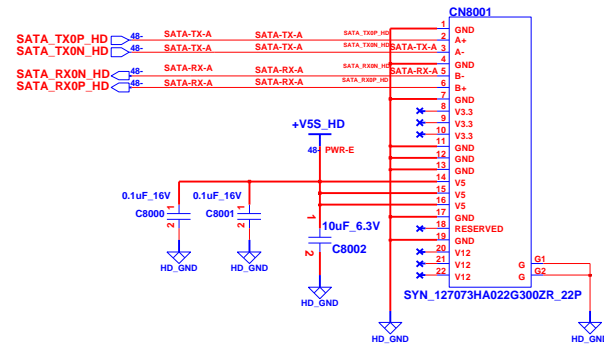
INVENTEC			
TITLE: RKY15HR			
USB, PowerButton (DB)			
SIZE: A3	CODE: CS	DOC. NUMBER: 1310A24121-0-MTR	REV: A01
SHEET: 47		OF: 58	

SATA HDD BOARD

SATA HDD BTB CONN on HDD/B 1A



HDD CONN



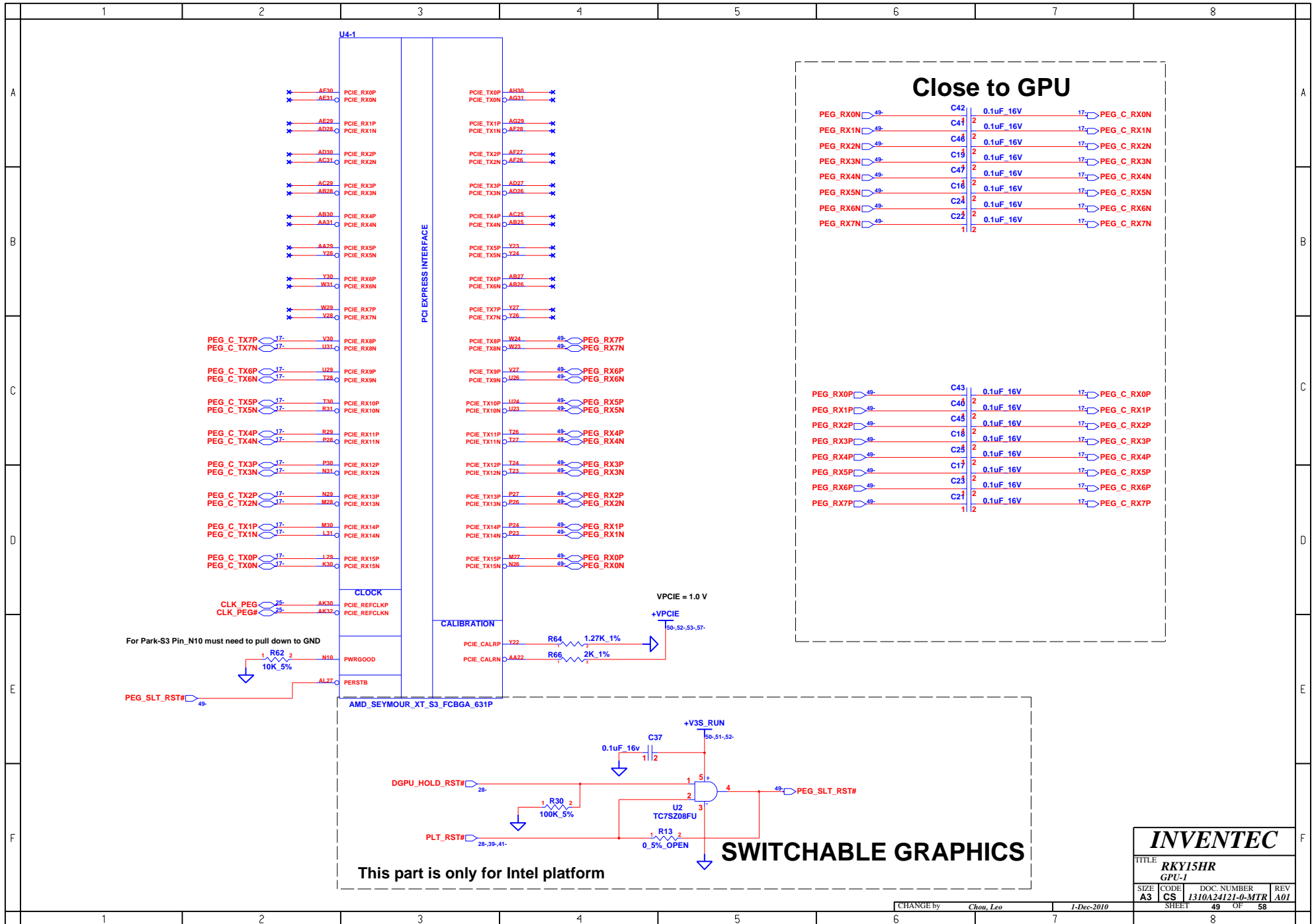
INVENTEC

TITLE
RKY15HR
HDD CONN

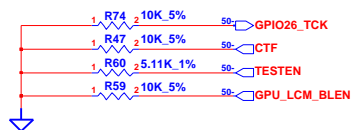
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A24121-0-MTR	A01

CHANGE by: Chou, Leo 1-Dec-2010

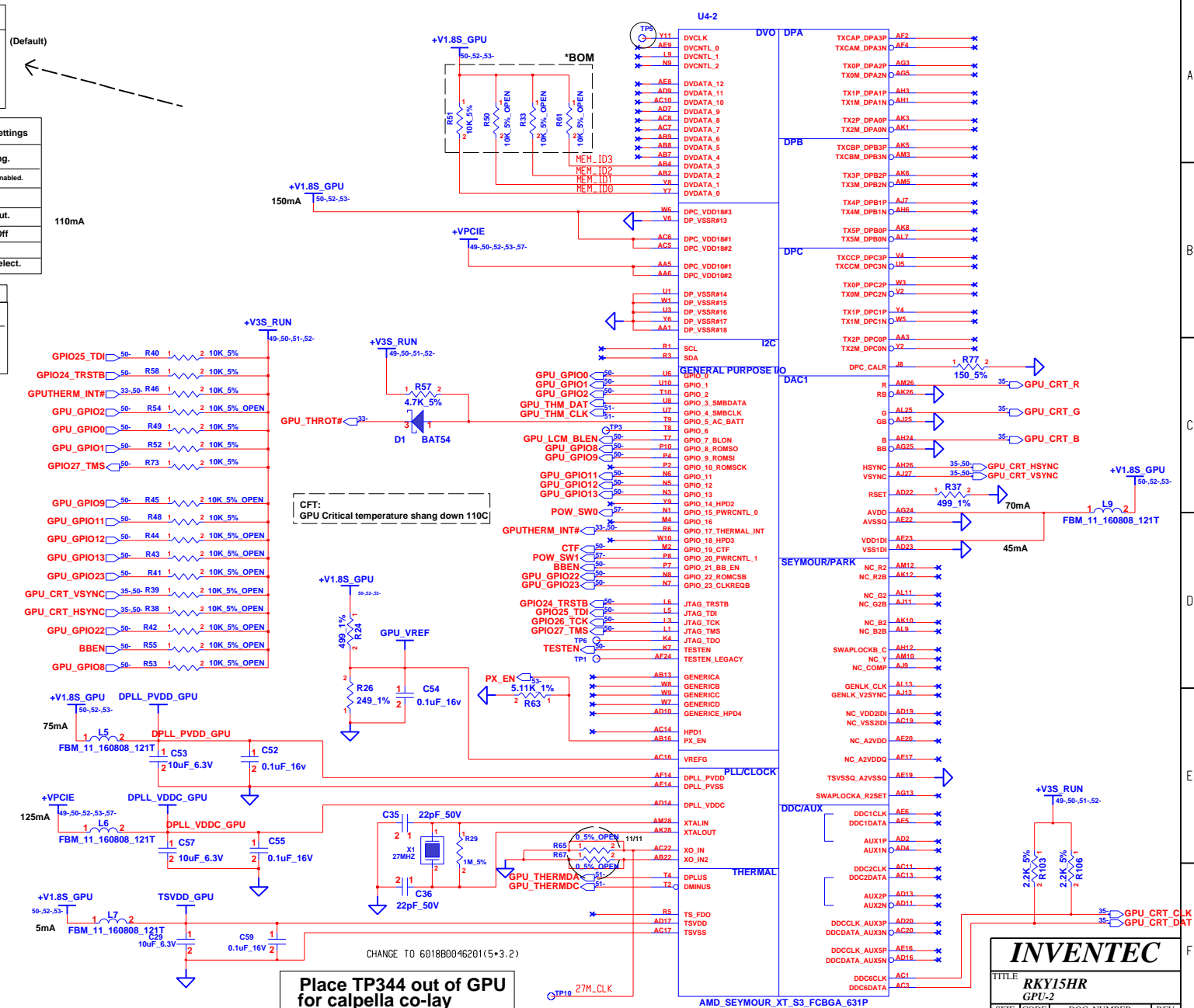
SHEET 48 OF 58



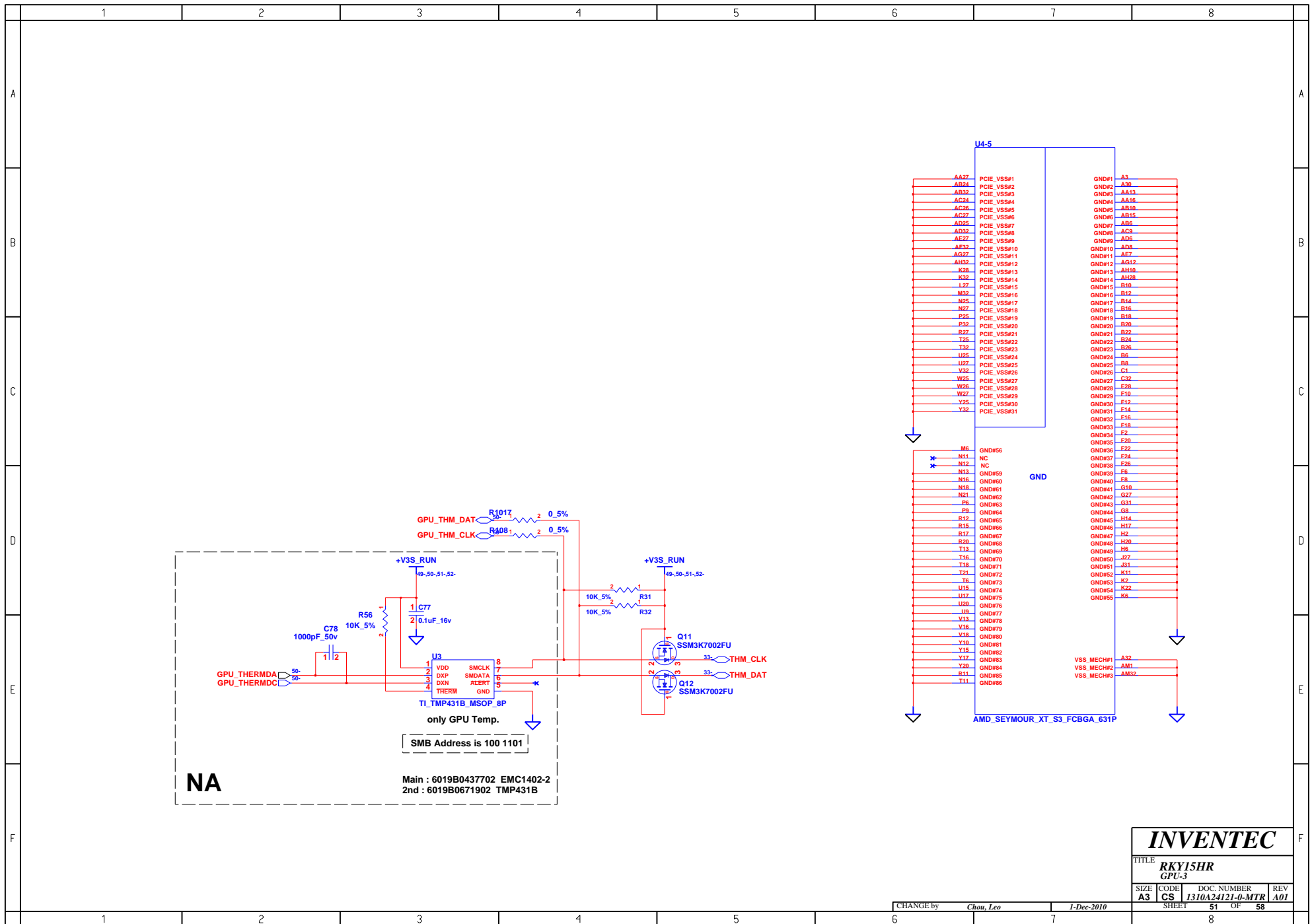
If GPIO_22_EN = 0, then GPIO[13:11] defines the primary memory aperture size.			
GPIO_13	GPIO_12	GPIO_11	Memory aperture size
0	0	1	512MB / 256MB (Default)
1	1	0	reserved

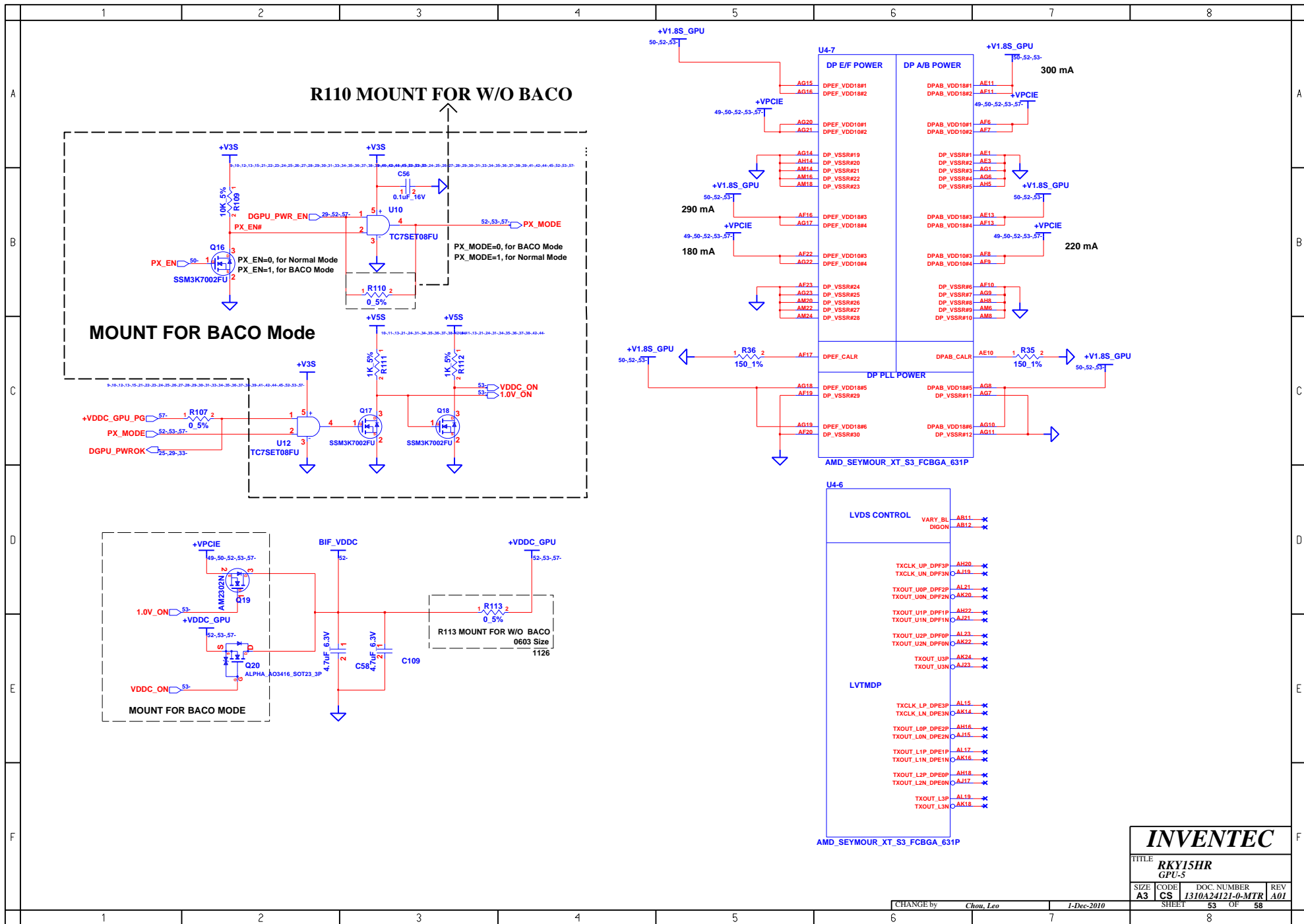


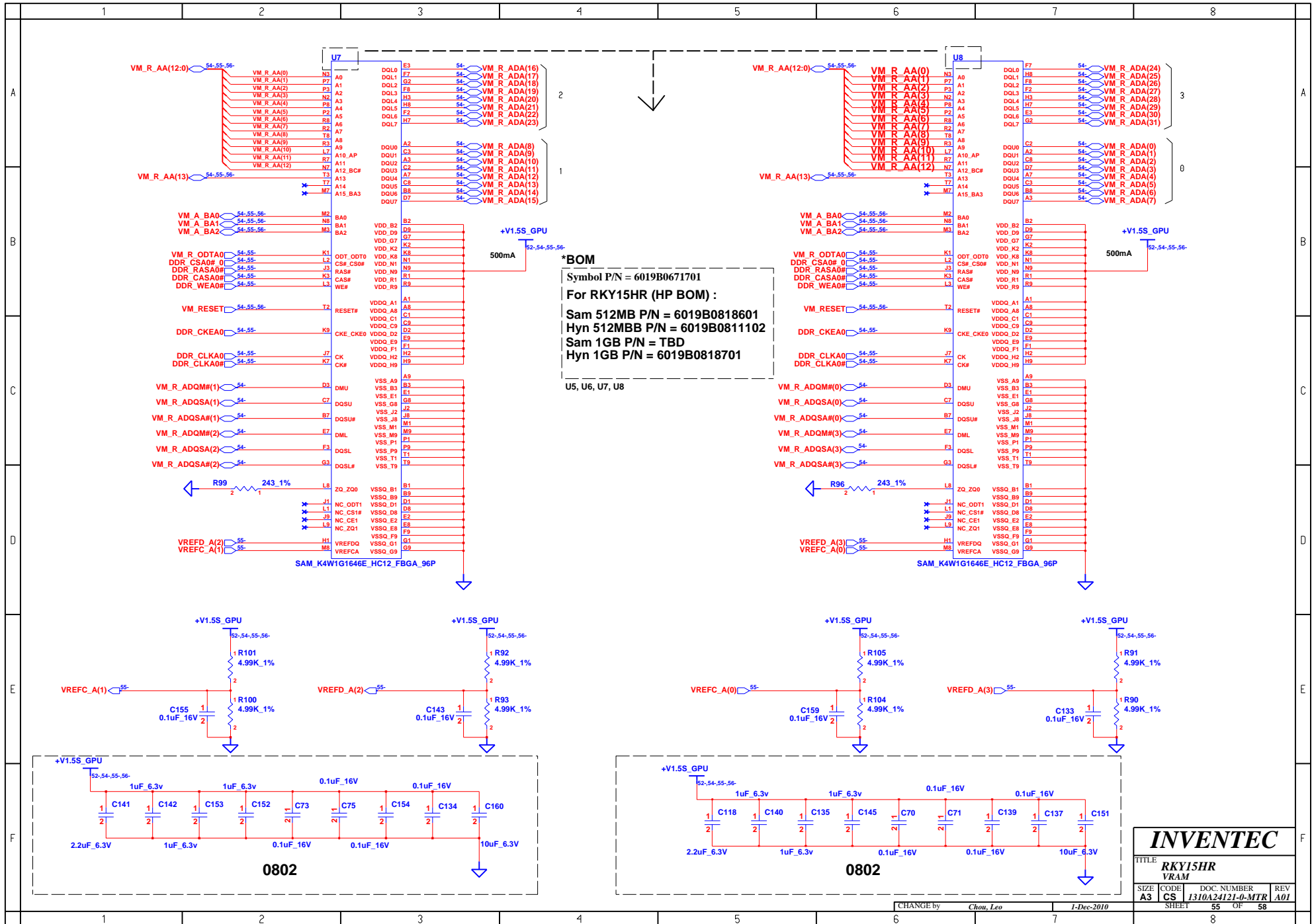
POW_SW1	POW_SW0	+VDDC_GPU
0	0	1.1V
0	1	1V
1	0	0.95V
1	1	0.9V

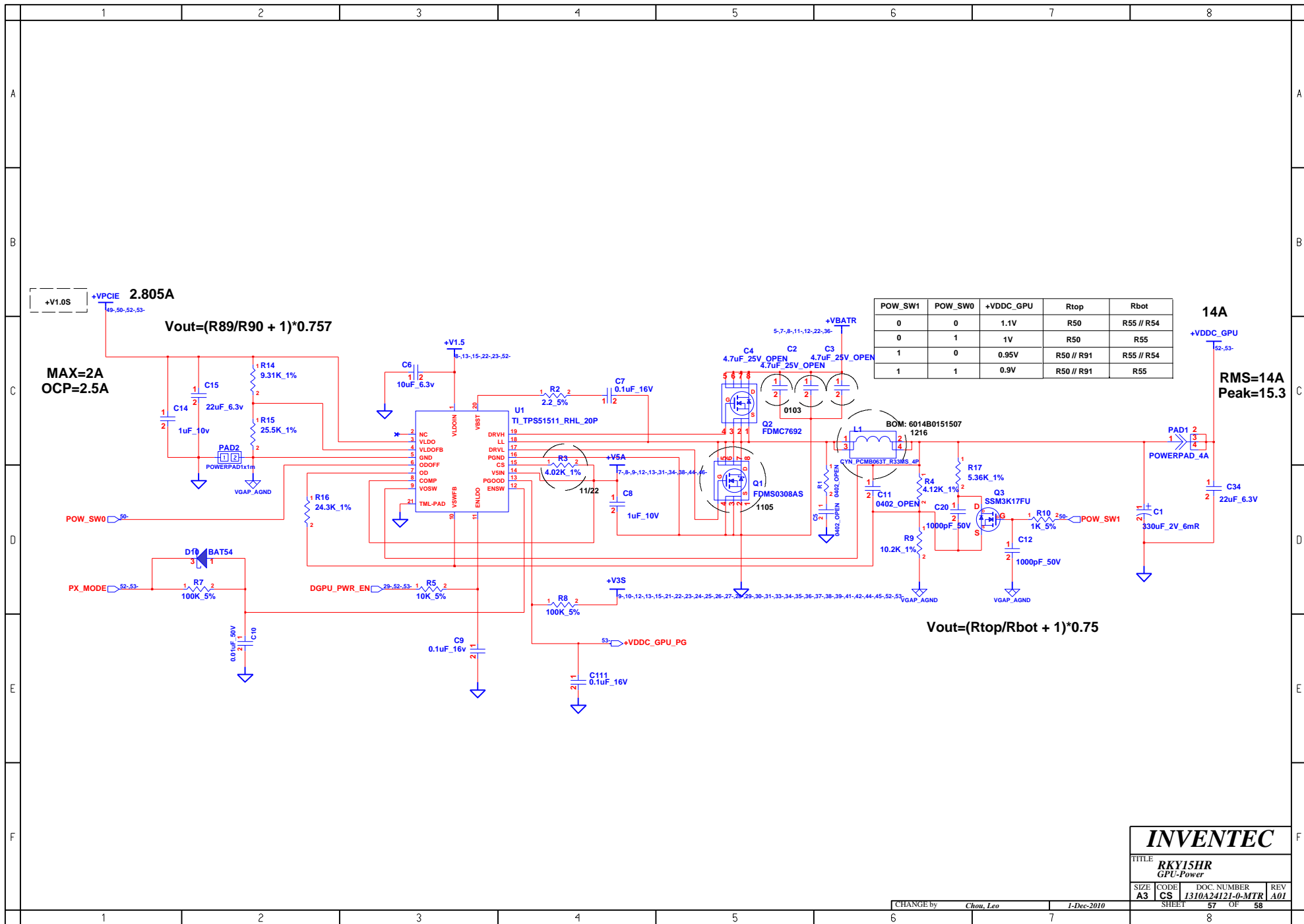


INVENTEC			
TITLE RKY15HR			
GPU-2			
SIZE A3	CODE CS	DOC. NUMBER 1310A24121-0-MTR	REV A01









	1	2	3	4	5	6	7	8	
A									
B									
C									
D									
E									
F									
CHANGE by						Chou, Leo		1-Dec-2010	
SIZE						CODE		DOC. NUMBER	REV
A3						CS		I310A24121-0-MTR	A01
SHEET						58		OF	58